Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology) 2023

ALL INDIA COUNCIL FOR TECHNICAL EDUCATION
Nelson Mandela Marg, Vasant Kunj, New Delhi 110070
www.aicte-india.org
Model Curriculum for
UG Degree Course
in
Electronics Engineering
(VLSI Design and Technology)
## Committee for Model Curriculum

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<tr>
<td>1</td>
<td>Dr. Siva Rama Krishna</td>
<td>Professor&lt;br&gt;Dept. of Electrical Engineering, IIT Hyderabad (Chairman)</td>
</tr>
<tr>
<td>2</td>
<td>Dr. Saurabh Lodha</td>
<td>Institute Chair Professor, Dept. of Electrical Engineering, IIT Bombay, Mumbai (Member)</td>
</tr>
<tr>
<td>3</td>
<td>Prof. Rajendra Patrikar</td>
<td>Professor, Centre for VLSI and Nano Technology, Visvesvaraya National Institute of Technology, Nagpur, Maharashtra (Member)</td>
</tr>
<tr>
<td>4</td>
<td>Dr. Sushobhan Avasthi</td>
<td>Associate Professor, Indian Institute of Science, Bengaluru (Member)</td>
</tr>
<tr>
<td>5</td>
<td>Dr. P. Ramesh</td>
<td>Associate Professor, Dept. of Electronics and Communication, Govt. College of Engg Munnar, Idukki, Kerala (Member)</td>
</tr>
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</table>
MESSAGE

The quality of technical education depends on many factors but largely on- outcome based social and industrially relevant curriculum, good quality motivated faculty, teaching learning process, effective industry internship and evaluation of students based on desired outcomes. Therefore, it was imperative that a Model Curriculum be prepared by best experts from academia and industry, keeping in view the latest industry trends and market requirements and be made available to all universities / board of technical education and engineering institutions in the country. AICTE constituted team of experts to prepare the model curriculum of UG Degree Course in Electronics Engineering (VLSI Design and Technology). Similar exercise is done for other UG, Diploma and PG level in engineering, MBA, PGDM, Architecture, etc.

It comprises of basic science and engineering courses, having focus on fundamentals, significant discipline level courses and ample electives both from the disciplines and cross disciplines including emerging areas all within a cumulative structure of 161 credits. Summer Internships have been embedded to make the student understand the industry requirements and have hands on experience. Virtual Labs has been introduced for few experiments. Also, most courses have been mapped to its equivalent SWAYAM/NPTEL Course to offer an alternative for learning that course online from SWAYAM. These features will allow students to develop a problem-solving approach to face the challenges in the future and develop outcome based learning approach.

As a major initiative by AICTE, a three-week mandatory induction program for students has also been designed and has to be given at the beginning of the course. The idea behind this is to make the students feel comfortable in their new environment, open them up, set a healthy daily routine, develop awareness, sensitivity and understanding of the self, people around them, society at large, and nature.

AICTE places on record, special thanks to Dr. Siva Rama Krishna, Dr. Saurabh Lodha, Prof. Rajendra Patrikar, Dr. Sushobhan Avasthi, Dr. P.Ramesh and other committee members. We are sure that this Model Curriculum will help to enhance not just the employability skills but will also enable youngsters to become job creators.

We strongly urge the institutions / universities / boards of technical education in India to adopt this Model Curriculum at the earliest. This is a suggestive curriculum and the concerned university / institution / board should build on and exercise flexibility in readjustment of courses within the overall 161 credits.

(Prof. T G Sitharam)
Chairman
All India Council for Technical Education
AICTE Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology)
PREFACE

Taking cognizance of growing concern about quality of technical education in India, AICTE in its 49th council meeting held on 14.03.2017 approved a package of measures for improving quality of technical education - Revision of Curriculum, Mandatory Internship, and Student Induction Program were amongst the few.

AICTE constituted committee of academia industry experts to prepare model curriculum of UG Course in Electronics Engineering (VLSI Design and Technology). During the development of curriculum, the employability and employment opportunities for graduates, future ready workforce who will be skilled enough to handle the rapid growth in the field of Semiconductor, VLSI Design and Technology were kept in mind.

AICTE has introduced mandatory internship in the new curriculum which will equip the students with practical understanding and training about industry practices in a suitable industry or organization. In the course of development of model curriculum, the committee took feedback of industry experts on the draft curriculum and accordingly modified the draft before finalization. This exercise has ensured that essential emphasis on industry requirements and market trends, employability and problem solving approach is given.

After due deliberations, the scheme and syllabus have been formulated. Salient features of this model curriculum are enumerated as under:

- Reduced number of credits.
- Introduction of Student Induction Program.
- Well defined learning objectives & outcomes for each course.
- Inclusion of courses on socially relevant topics.
- Built-in flexibility to the students in terms of professional elective and open elective courses.
- Mandatory internship to equip the students with practical knowledge and provide them exposure to real time industrial environments.
- Virtual Labs.
- Mapping of Courses to its equivalent NPTEL/SWAYAM Course.
- Course on ‘Entrepreneurship and Startups’ to encourage entrepreneurial mindset.
- Introduction of Design Thinking and Universal Human Value course.

Thanks for the time and efforts of the members of the working group Chaired by Prof. Siva Rama Krishna and which included, Dr. Saurabh Lodha, Prof. Rajendra Patrikar, Dr. Sushobhan Avasthi, Dr. P.Ramesh and other committee members.

Special thanks to Prof. T G Sitharam, Chairman; Dr. Abhay Jere, Vice- Chairman and Prof. Rajive Kumar, Member Secretary, AICTE who all have been instrumental and
encouraging throughout the process of development of this model curriculum, and the dedicated efforts of Dr. Naveen Arora, Assistant Director (P&AP); Dr. Anil Sharma, Assistant Director (P&AP), Mr. Rakesh Kumar Pandit, Young Professional (P&AP); and other office staff of AICTE.

Dr. Ramesh Unnikrishnan
Advisor – II (P&AP)
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</tr>
<tr>
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<tr>
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AICTE Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology)
GENERAL COURSE STRUCTURE
& CREDIT DISTRIBUTION
GENERAL COURSE STRUCTURE & THEME

A. Definition of Credit:

<table>
<thead>
<tr>
<th>Category</th>
<th>Credit Breakup for VLSI Design &amp; Tech</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 Hr. Lecture (L) per week</td>
<td>1 Credit</td>
</tr>
<tr>
<td>1 Hr. Tutorial (T) per week</td>
<td>1 Credit</td>
</tr>
<tr>
<td>1 Hr. Practical (P) per week</td>
<td>0.5 Credit</td>
</tr>
<tr>
<td>2 Hours Practical (P) per week</td>
<td>1 Credit</td>
</tr>
</tbody>
</table>

B. Range of Credits: In the light of the fact that a typical Model Four-year Under Graduate degree program in Engineering has about 163 credits, the total number of credits proposed for the four-year B. Tech/B.E. in Electronics Engineering (VLSI Design and Technology) (Engineering & Technology) is kept as 161.

C. Structure of UG Program in VLSI Design and Technology: The structure of UG program in Electronics Engineering (VLSI Design and Technology) shall have essentially the following categories of courses with the breakup of credits as given:

<table>
<thead>
<tr>
<th>S.No.</th>
<th>Category</th>
<th>Credit Breakup for VLSI Design &amp; Tech</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Humanities and Social Sciences including Management courses</td>
<td>15</td>
</tr>
<tr>
<td>2</td>
<td>Basic Science courses</td>
<td>23</td>
</tr>
<tr>
<td>3</td>
<td>Engineering Science courses including workshop, drawing, basics of electronics/electrical/mechanical/computer etc.</td>
<td>17</td>
</tr>
<tr>
<td>4</td>
<td>Professional core courses</td>
<td>64</td>
</tr>
<tr>
<td>5</td>
<td>Professional Elective courses relevant to chosen specialization/branch</td>
<td>12</td>
</tr>
<tr>
<td>6</td>
<td>Open subjects – Electives from other technical and/or emerging subjects</td>
<td>12</td>
</tr>
<tr>
<td>7</td>
<td>Project work, seminar and internship in industry or elsewhere</td>
<td>18</td>
</tr>
<tr>
<td>8</td>
<td>Mandatory Courses [Environmental Sciences, Induction Program, Indian Constitution, Essence of Indian Knowledge Tradition]</td>
<td>(non-credit)</td>
</tr>
</tbody>
</table>

Total 161*

*Minor variation is allowed as per need of the respective disciplines.

D. Course code and definition:

<table>
<thead>
<tr>
<th>Course code</th>
<th>Definitions</th>
</tr>
</thead>
<tbody>
<tr>
<td>L</td>
<td>Lecture</td>
</tr>
<tr>
<td>T</td>
<td>Tutorial</td>
</tr>
<tr>
<td>P</td>
<td>Practical</td>
</tr>
<tr>
<td>C</td>
<td>Credits</td>
</tr>
<tr>
<td>BSC</td>
<td>Basic Science Courses</td>
</tr>
<tr>
<td>ESC</td>
<td>Engineering Science Courses</td>
</tr>
<tr>
<td>HSMC</td>
<td>Humanities and Social Sciences including Management courses</td>
</tr>
<tr>
<td>PCC-CS</td>
<td>Professional core courses</td>
</tr>
</tbody>
</table>
PEC -CS | Professional Elective courses
---|---
OEC-CS | Open Elective courses
LC | Laboratory course
MC | Mandatory courses

- **Course level coding scheme:** Three-digit number (odd numbers are for the odd semester courses and even numbers are for even semester courses) used as suffix with the Course Code for identifying the level of the course. Digit at hundred’s place signifies the year in which course is offered. e.g.

  101, 102 … etc. for first year.
  201, 202 …. Etc. for second year.
  301, 302 … for third year.

- **Category-wise Courses**

  **HUMANITIES & SOCIAL SCIENCES COURSES [HSM]**

  (i) Number of Humanities & Social Science Courses: 5
  (ii) Credits: 15

<table>
<thead>
<tr>
<th>Sl. No</th>
<th>Code No.</th>
<th>Course Title</th>
<th>Semester</th>
<th>Hours per week</th>
<th>Total Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Lecture</td>
<td>Tutorial</td>
</tr>
<tr>
<td>1</td>
<td>HSM-01</td>
<td>English for Technical Writing</td>
<td>I</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>HSM-02</td>
<td>Universal Human Values- II: Understanding Harmony And Ethical Human Conduct</td>
<td>II</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>4</td>
<td>HSM-03</td>
<td>Management-I (Organizational Behaviour)/ Finance &amp; Accounting</td>
<td>IV</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>HSM-04</td>
<td>Humanities – I</td>
<td>V</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>HSM-05</td>
<td>Humanities – II</td>
<td>VII</td>
<td>3</td>
<td>0</td>
</tr>
</tbody>
</table>

**Total Credits** 15

*****
### BASIC SCIENCE COURSE [BSC]

<table>
<thead>
<tr>
<th>Sl. No</th>
<th>Code No.</th>
<th>Course Title</th>
<th>Semester</th>
<th>Hours per week</th>
<th>Total Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Lecture</td>
<td>Tutorial</td>
</tr>
<tr>
<td>1</td>
<td>BS-01</td>
<td>Physics-I (Introduction to Electromagnetic Theory)</td>
<td>I</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>BS-02</td>
<td>Mathematics-I (Calculus and Linear Algebra)</td>
<td>I</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>BS-03</td>
<td>Chemistry-I</td>
<td>II</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>4</td>
<td>BS-04</td>
<td>Mathematics-II (Probability and Statistics)</td>
<td>II</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>5</td>
<td>BS-05</td>
<td>Biology for Engineers</td>
<td>II</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>BS-05</td>
<td>Slot for BS</td>
<td>III</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>Total Credits</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### ENGINEERING SCIENCE COURSE [ESC]

<table>
<thead>
<tr>
<th>Sl. No</th>
<th>Code No.</th>
<th>Course Title</th>
<th>Semester</th>
<th>Hours per week</th>
<th>Total Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Lecture</td>
<td>Tutorial</td>
</tr>
<tr>
<td>1</td>
<td>ES-01</td>
<td>Basic Electrical Engineering</td>
<td>I</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>2</td>
<td>ES-02</td>
<td>Engineering Graphics &amp; Design</td>
<td>I</td>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>ES-03</td>
<td>Design Thinking</td>
<td>I</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>4</td>
<td>ES-04</td>
<td>Programming for Problem Solving</td>
<td>II</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>ES-05</td>
<td>Digital Fabrication / Workshop/Manufacturing Practices</td>
<td>II</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>ES-06</td>
<td>Numerical Techniques</td>
<td>IV</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>Total Credits</strong></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### PROFESSIONAL CORE COURSES [PCC]

<table>
<thead>
<tr>
<th>Sr. No</th>
<th>Course Code</th>
<th>Course Title</th>
<th>Preferred Semester</th>
<th>Hrs /Week L: T: P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>XC01</td>
<td>Electronic Devices</td>
<td>III</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>XC02</td>
<td>Electronics Devices Lab</td>
<td>III</td>
<td>0:0:2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>XC03</td>
<td>Digital System Design</td>
<td>III</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>Course Code</td>
<td>Course Title</td>
<td>Term</td>
<td>Credits</td>
<td>Lab Credits</td>
<td></td>
</tr>
<tr>
<td>-------------</td>
<td>--------------------------------------------------</td>
<td>------</td>
<td>---------</td>
<td>-------------</td>
<td></td>
</tr>
<tr>
<td>XC04</td>
<td>Digital System Design Lab</td>
<td>III</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC05</td>
<td>Signals and Systems</td>
<td>III</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC06</td>
<td>Network Theory</td>
<td>III</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC07</td>
<td>Probability Theory and Stochastic Processes</td>
<td>III</td>
<td>3:1:0</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>XC-08</td>
<td>Analog Circuits</td>
<td>IV</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-09</td>
<td>Analog Circuits Lab</td>
<td>IV</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-10</td>
<td>Microcontrollers and Computer Architecture</td>
<td>IV</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-11</td>
<td>Microcontrollers Lab</td>
<td>IV</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-12</td>
<td>Analog and Digital Communication</td>
<td>IV</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-13</td>
<td>Introduction to Microfabrication</td>
<td>IV</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-14</td>
<td>Introduction to Microfabrication Lab {</td>
<td>IV</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Foundry Familiarization workshop / MOOC virtual lab}</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>XC-15</td>
<td>Introduction to VLSI lifecycle</td>
<td>IV</td>
<td>0:0:1</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-16</td>
<td>Digital Signal Processing</td>
<td>V</td>
<td>3:1:0</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>XC-17</td>
<td>Electromagnetic Waves</td>
<td>V</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-18</td>
<td>Electromagnetic Waves Lab</td>
<td>V</td>
<td>3:1:0</td>
<td>4</td>
<td></td>
</tr>
<tr>
<td>XC-19</td>
<td>VLSI Design</td>
<td>V</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-20</td>
<td>VLSI Design Lab</td>
<td>V</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-21</td>
<td>Control Systems</td>
<td>V</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-22</td>
<td>Embedded Systems</td>
<td>V</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-23</td>
<td>Embedded Systems Lab</td>
<td>V</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-24</td>
<td>VLSI Verification and Testing</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-25</td>
<td>VLSI Verification and Testing Lab</td>
<td>VI</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-26</td>
<td>Semiconductor Equipment Design and Technology</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-27</td>
<td>Semiconductor Materials Synthesis and Characterization</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
<td></td>
</tr>
<tr>
<td>XC-28</td>
<td>Seminar</td>
<td>VII</td>
<td>0:0:2</td>
<td>1</td>
<td></td>
</tr>
<tr>
<td>XC-29</td>
<td>Internship /Project Phase-1</td>
<td>VII</td>
<td>0:0:4</td>
<td>2</td>
<td></td>
</tr>
</tbody>
</table>

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### PROFESSIONAL ELECTIVE [PE]

<table>
<thead>
<tr>
<th>S. No</th>
<th>Code No.</th>
<th>Course Title</th>
<th>Semester</th>
<th>Hours per week</th>
<th>Total Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Lecture</td>
<td>Tutorial</td>
</tr>
<tr>
<td>1</td>
<td>PE</td>
<td>Elective – I</td>
<td>VI</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>PE</td>
<td>Elective - II</td>
<td>VII</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>PE</td>
<td>Elective-III</td>
<td>VII</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>4</td>
<td>PE</td>
<td>Elective-IV</td>
<td>VIII</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>Total</strong></td>
<td></td>
</tr>
</tbody>
</table>

### OPEN ELECTIVE COURSES [OE]

<table>
<thead>
<tr>
<th>S.No</th>
<th>Code No.</th>
<th>Course Title</th>
<th>Semester</th>
<th>Hours per week</th>
<th>Total Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Lecture</td>
<td>Tutorial</td>
</tr>
<tr>
<td>1</td>
<td>OE</td>
<td>Open Elective – I</td>
<td>VI</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>OE</td>
<td>Open-Elective-II</td>
<td>VII</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>OE</td>
<td>Open-Elective-III</td>
<td>VII</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>4</td>
<td>OE</td>
<td>Open-Elective-IV</td>
<td>VIII</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### PROJECT WORK, SEMINAR AND INTERNSHIP IN INDUSTRY OR ELSEWHERE

<table>
<thead>
<tr>
<th>Sl. No</th>
<th>Course Code</th>
<th>Course Title</th>
<th>Semester</th>
<th>Hours per week</th>
<th>Total Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Lecture</td>
<td>Tutorial</td>
</tr>
<tr>
<td>1</td>
<td>XC-P1</td>
<td>Micro Project</td>
<td>IV</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>XC-P2</td>
<td>Mini Project</td>
<td>VI</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>3</td>
<td>XC-28</td>
<td>Seminar</td>
<td>VII</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>4</td>
<td>XC-29</td>
<td>Internship/ Project Phase-1</td>
<td>VII</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>5</td>
<td>XC-P3</td>
<td>Project</td>
<td>VIII</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*****
INDUCTION PROGRAM

The Essence and Details of Induction program can also be understood from the ‘Detailed Guide on Student Induction program’, as available on AICTE Portal, (Link: https://www.aicteindia.org/sites/default/files/Detailed%20Guide%20on%20Student%20Induction%20program.pdf). For more, Refer Appendix II.

<table>
<thead>
<tr>
<th>Induction program (mandatory)</th>
<th>Three-week duration</th>
</tr>
</thead>
</table>
| Induction program for students to be offered right at the start of the first year. | • Physical activity  
• Creative Arts  
• Universal Human Values  
• Literary  
• Proficiency Modules  
• Lectures by Eminent People  
• Visits to local Areas  
• Familiarization to Dept./Branch & Innovations |

E. Mandatory Visits/ Workshop/Expert Lectures:

a. It is mandatory to arrange one industrial visit every semester for the students of each branch.

b. It is mandatory to conduct a One-week workshop during the winter break after fifth semester on professional/ industry/ entrepreneurial orientation.

c. It is mandatory to organize at least one expert lecture per semester for each branch by inviting resource persons from domain specific industry.

F. Evaluation Scheme (Suggestive only):

a. For Theory Courses:
   (The weightage of Internal assessment is 40% and for End Semester Exam is 60%)
   The student has to obtain at least 40% marks individually both in internal assessment and end semester exams to pass.

b. For Practical Courses:
   (The weightage of Internal assessment is 60% and for End Semester Exam is 40%)
   The student has to obtain at least 40% marks individually both in internal assessment and end semester exams to pass.

c. For Summer Internship / Projects / Seminar etc.
   Evaluation is based on work done, quality of report, performance in viva-voce, presentation etc.

Note: The internal assessment is based on the student’s performance in mid semester tests (two best out of three), quizzes, assignments, class performance, attendance, viva-voce in practical, lab record etc.
G. Mapping of Marks to Grades

Each course (Theory/Practical) is to be assigned 100 marks, irrespective of the number of credits, and the mapping of marks to grades may be done as per the following table:

<table>
<thead>
<tr>
<th>Range of Marks</th>
<th>Assigned Grade</th>
</tr>
</thead>
<tbody>
<tr>
<td>91-100</td>
<td>AA/A⁺</td>
</tr>
<tr>
<td>81-90</td>
<td>AB/A</td>
</tr>
<tr>
<td>71-80</td>
<td>BB/B⁺</td>
</tr>
<tr>
<td>61-70</td>
<td>BC/B</td>
</tr>
<tr>
<td>51-60</td>
<td>CC/C⁺</td>
</tr>
<tr>
<td>46-50</td>
<td>CD/C</td>
</tr>
<tr>
<td>40-45</td>
<td>DD/D</td>
</tr>
<tr>
<td>&lt; 40</td>
<td>FF/F (Fail due to less marks)</td>
</tr>
<tr>
<td>-</td>
<td>FR (Fail due to shortage of attendance and therefore, to repeat the course)</td>
</tr>
</tbody>
</table>

******
Bridge Courses

A After First Year:
The candidate should pass following two additional courses (ITI Level) to qualify for Certification.

1. Electronic Measurements and Instrumentation
2. Clean Room Technologies

B After Second Year:
The candidate should pass following two additional courses in either design stream or manufacturing stream (Diploma Level) to qualify for Diploma.

1. Embedded Systems
2. VLSI Design

OR

1. Semiconductor Equipment Design and Technology
2. Semiconductor Materials Synthesis and Characterization

C After Third Year:
The candidate should pass following additional courses in either design stream or manufacturing stream (Diploma Level) to qualify for B.Voc

1. Analog IC Design
2. Power Converter Design

OR

1. Semiconductor Packaging and Testing
2. Introduction to MEMS
SEMESTER WISE STRUCTURE
## SEMESTER I

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>BS-01</td>
<td>Physics (Introduction to Electromagnetic Theory)</td>
<td>3</td>
<td>1</td>
<td>2</td>
<td>5</td>
</tr>
<tr>
<td>2</td>
<td>BS-02</td>
<td>Mathematics-I</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>3</td>
<td>ES-01</td>
<td>Basic Electrical Engineering</td>
<td>2</td>
<td>1</td>
<td>2</td>
<td>4</td>
</tr>
<tr>
<td>4</td>
<td>ES-02</td>
<td>Engineering Graphics &amp; Design</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>HSM-01</td>
<td>English for Technical Writing</td>
<td>2</td>
<td>0</td>
<td>2</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td>ES-03</td>
<td>Design Thinking</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>7</td>
<td>AU-01^</td>
<td>IDEA Lab Workshop</td>
<td>2</td>
<td>0</td>
<td>4</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>TOTAL</td>
<td></td>
<td>13</td>
<td>3</td>
<td>16</td>
<td>20</td>
</tr>
</tbody>
</table>

### SEMESTER II

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>BS-03</td>
<td>Chemistry</td>
<td>3</td>
<td>0</td>
<td>2</td>
<td>4</td>
</tr>
<tr>
<td>2</td>
<td>BS-04</td>
<td>Mathematics-II</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>3</td>
<td>ES-04</td>
<td>Programming for Problem Solving</td>
<td>2</td>
<td>0</td>
<td>4</td>
<td>4</td>
</tr>
<tr>
<td>4</td>
<td>BS-05</td>
<td>Biology for Engineers</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>ES-05</td>
<td>Digital Fabrication / Workshop/Manufacturing</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>6</td>
<td>HSM-02</td>
<td>Universal Human Values</td>
<td>2</td>
<td>1</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td>AU-02^</td>
<td>Sports and Yoga or NSS/NCC</td>
<td>2</td>
<td>0</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td></td>
<td>TOTAL</td>
<td></td>
<td>15</td>
<td>2</td>
<td>10</td>
<td>20</td>
</tr>
</tbody>
</table>

^ represent “Audit Course”.

15
### SEMESTER-III

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>XC-01</td>
<td>Electronic Devices</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>XC-02</td>
<td>Electronic Devices Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>XC-03</td>
<td>Digital System Design</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>XC-04</td>
<td>Digital System Design Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>5</td>
<td>XC-05</td>
<td>Signals and Systems</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td>XC-06</td>
<td>Network Theory</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td>XC-07</td>
<td>Probability Theory and Stochastic Processes</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>8</td>
<td>BS-06</td>
<td>Slot for BS Course</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>ML-01</td>
<td>Slot for Mandatory Learning Course (Audit Course)</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>TOTAL</strong></td>
<td>21</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### SEMESTER-IV

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>XC-08</td>
<td>Analog Circuits</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>XC-09</td>
<td>Analog Circuits Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>XC-10</td>
<td>Microcontrollers and Computer Architecture</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>XC-11</td>
<td>Microcontrollers Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>5</td>
<td>XC-12</td>
<td>Analog and Digital Communication</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td>XC-13</td>
<td>Introduction to Microfabrication</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td>XC-14</td>
<td>Introduction to Microfabrication Lab { Foundry Familiarization workshop / MOOC virtual lab}</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>8</td>
<td>XC-15</td>
<td>Introduction to VLSI lifecycle</td>
<td>1</td>
<td>0</td>
<td>0</td>
<td>1</td>
</tr>
<tr>
<td>9</td>
<td>XC-P1</td>
<td>Micro Project</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td>10</td>
<td>ES-06</td>
<td>Numerical Techniques</td>
<td>2</td>
<td>0</td>
<td>2</td>
<td>3</td>
</tr>
<tr>
<td>11</td>
<td>HSM-03</td>
<td>Slot for HM Course</td>
<td>3</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>ML-02</td>
<td>Slot for Mandatory Learning Course (Audit Course)</td>
<td>0</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td><strong>TOTAL</strong></td>
<td>24</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
### SEMESTER-V

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>XC-16</td>
<td>Digital Signal Processing</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>2</td>
<td>XC-17</td>
<td>Electromagnetic Waves</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
</tr>
<tr>
<td>3</td>
<td>XC-18</td>
<td>Electromagnetic Waves Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>4</td>
<td>XC-19</td>
<td>VLSI Design</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>XC-20</td>
<td>VLSI Design Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>6</td>
<td>XC-21</td>
<td>Control Systems</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td>XC-22</td>
<td>Embedded Systems</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>8</td>
<td>XC-23</td>
<td>Embedded Systems Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>9</td>
<td>HSM-04</td>
<td>Slot for HM Course</td>
<td></td>
<td></td>
<td></td>
<td>3</td>
</tr>
<tr>
<td>10</td>
<td>ML-03</td>
<td>Slot for Mandatory Learning Course (Audit Course)</td>
<td></td>
<td></td>
<td></td>
<td>0</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>23</strong></td>
</tr>
</tbody>
</table>

### SEMESTER-VI

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>XC-24</td>
<td>VLSI Verification and Testing</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>XC-25</td>
<td>VLSI Verification and Testing Lab</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>XC-26</td>
<td>Semiconductor Equipment Design and Technology</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>XC-27</td>
<td>Semiconductor Materials Synthesis and Characterization</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td>PE-1</td>
<td>Program Elective-1</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td>OE-1</td>
<td>Open Elective-1</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>8</td>
<td>XC-P2</td>
<td>Mini Project</td>
<td>0</td>
<td>0</td>
<td>6</td>
<td>3</td>
</tr>
<tr>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>19</strong></td>
</tr>
</tbody>
</table>
## SEMESTER-VII

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PE-2</td>
<td>Program Elective-2</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>PE-3</td>
<td>Program Elective-3</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>3</td>
<td>OE-2</td>
<td>Open Elective-2</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>OE-3</td>
<td>Open Elective-3</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>HSM-5</td>
<td>Slot for HSM Course</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td>XC-28</td>
<td>Seminar</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>7</td>
<td>XC-29</td>
<td>Internship /Project Phase -1</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>2</td>
</tr>
<tr>
<td></td>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>18</strong></td>
</tr>
</tbody>
</table>

## SEMESTER-VIII

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Credit</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PE-04</td>
<td>Program Elective-4</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>OE-04</td>
<td>Open Elective-4</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>3</td>
<td>XC-P3</td>
<td>Project</td>
<td>0</td>
<td>0</td>
<td>20</td>
<td>10</td>
</tr>
<tr>
<td></td>
<td><strong>TOTAL</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td><strong>16</strong></td>
</tr>
</tbody>
</table>

(XCEL*: Course to be selected from the list of Program Electives)
SEMESTER – I
AICTE Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology)

SEMESTER I

<table>
<thead>
<tr>
<th>Course Code</th>
<th>BS -01</th>
</tr>
</thead>
<tbody>
<tr>
<td>Course Title</td>
<td>Physics- I</td>
</tr>
<tr>
<td>Number of Credits</td>
<td>5 (L: 3, T: 1, P: 2)</td>
</tr>
<tr>
<td>Course Category</td>
<td>Basic Science Courses</td>
</tr>
<tr>
<td>Course Contents in Physics</td>
<td>i. Introduction to Electromagnetic Theory</td>
</tr>
</tbody>
</table>

**Course Objectives:** To enhance the fundamental knowledge in Physics and its applications relevant to various streams of Engineering and Technology.

**1. Introduction to Electromagnetic Theory**

Pre-requisites (if any): Mathematics course with vector calculus

**Module I: Electrostatics in vacuum**
Calculation of electric field and electrostatic potential for a charge distribution; Divergence and curl of electrostatic field; Laplace’s and Poisson’s equations for electrostatic potential and uniqueness of their solution and connection with steady state diffusion and thermal conduction; Practical examples like Faraday’s cage and coffee-ring effect; Boundary conditions of electric field and electrostatic potential; method of images; energy of a charge distribution and its expression in terms of electric field.

**Module II: Electrostatics in a linear dielectric medium**
Electrostatic field and potential of a dipole. Bound charges due to electric polarization; Electric displacement; boundary conditions on displacement; Solving simple electrostatics problems in presence of dielectrics – Point charge at the centre of a dielectric sphere, charge in front of a dielectric slab, dielectric slab and dielectric sphere in uniform electric field.

**Module III: Magnetostatics**
Bi-Savart law, Divergence and curl of static magnetic field; vector potential and calculating it for a given magnetic field using Stokes’ theorem; the equation for the vector potential and its solution for given current densities.

**Module IV: Magnetostatics in a linear magnetic medium**
Magnetization and associated bound currents; auxiliary magnetic field H; Boundary conditions on B and H. Solving for magnetic field due to simple magnets like a bar magnet; magnetic susceptibility and ferromagnetic, paramagnetic and diamagnetic materials; Qualitative discussion of magnetic field in presence of magnetic materials.

**Module V: Faraday’s law**
Faraday’s law in terms of EMF produced by changing magnetic flux; equivalence of Faraday’s law and motional EMF; Lenz’s law; Electromagnetic breaking and its applications; Differential form of
Faraday’s law expressing curl of electric field in terms of time-derivative of magnetic field and calculating electric field due to changing magnetic fields in quasi-static approximation; energy stored in a magnetic field.

**Module VI: Displacement current, Magnetic field due to time-dependent electric field and Maxwell’s equations**
Continuity equation for current densities; Modifying equation for the curl of magnetic field to satisfy continuity equation; displace current and magnetic field arising from time dependent electric field; calculating magnetic field due to changing electric fields in quasistatic approximation. Maxwell’s equation in vacuum and non-conducting medium; Energy in an electromagnetic field; Flow of energy and Pointing vector with examples. Qualitative discussion of momentum in electromagnetic fields.

**Module VII: Electromagnetic waves**
The wave equation; Plane electromagnetic waves in vacuum, their transverse nature and polarization; relation between electric and magnetic fields of an electromagnetic wave; energy carried by electromagnetic waves and examples. Momentum carried by electromagnetic waves and resultant pressure. Reflection and transmission of electromagnetic waves from a non-conducting medium-vacuum interface for normal incidence.

**Laboratory - Introduction to Electromagnetic Theory**
Choice of experiments from the following:
- Experiments on electromagnetic induction and electromagnetic braking;
- LC circuit and LCR circuit;
- Resonance phenomena in LCR circuits;
- Magnetic field from Helmholtz coil;
- Measurement of Lorentz force in a vacuum tube.

**TEXTBOOKS/REFERENCES:**
2. David Griffiths, Introduction to Electrodynamics
3. Halliday and Resnick, Physics
4. W. Saslow, Electricity, magnetism and light

**Alternative NPTEL/SWAYAM Course:**

<table>
<thead>
<tr>
<th>S. No.</th>
<th>NPTEL Course Name</th>
<th>Instructor</th>
<th>Host Institute</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>INTRODUCTION ELECTROMAGNETIC THEORY</td>
<td>PROF. MANOJ</td>
<td>IIT KANPUR</td>
</tr>
<tr>
<td></td>
<td></td>
<td>HARBOLA</td>
<td></td>
</tr>
</tbody>
</table>
EXPERIMENTS THAT MAY BE PERFORMED THROUGH VIRTUAL LABS:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Experiment Name</th>
<th>Experiment Link(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>LC circuit and LCR circuit;</td>
<td>1. <a href="http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=326&amp;cnt=1">http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=326&amp;cnt=1</a></td>
</tr>
<tr>
<td></td>
<td></td>
<td>2. <a href="http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=330&amp;cnt=1">http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=330&amp;cnt=1</a></td>
</tr>
<tr>
<td></td>
<td></td>
<td>3. <a href="http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=318&amp;cnt=1">http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=318&amp;cnt=1</a></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4. <a href="http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=325&amp;cnt=1">http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=325&amp;cnt=1</a></td>
</tr>
<tr>
<td>2</td>
<td>Resonance phenomena in LCR circuits</td>
<td><a href="http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=325&amp;cnt=1">http://vlab.amrita.edu/?sub=1&amp;brch=75&amp;sim=325&amp;cnt=1</a></td>
</tr>
</tbody>
</table>

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Course Code : BS-02
Course Title : Mathematics- I
Number of Credits : 4 (L: 3, T: 1, P: 0)
Course Category : Basic Science Courses

Course Objectives: The goal of this course is to achieve conceptual understanding and to retain the best traditions of traditional calculus. The syllabus is designed to provide the basic tools of calculus mainly for the purpose of modelling the engineering problems mathematically and obtaining solutions. This is a foundation course which mainly deals with topics such as single variable and multivariable calculus and plays an important role in the understanding of science, engineering, economics and computer science, among other disciplines.

Course Contents:

Module 1: Basic Calculus: (6 hours)
Curvature, evolutes and involutes; Evaluation of definite and improper integrals; Beta and Gamma functions and their properties; Applications of definite integrals to evaluate surface areas and volumes of revolutions.

Module 2: Single-variable Calculus (Differentiation): (6 hours)
Rolle’s Theorem, Mean value theorems and applications; Extreme values of functions; Linear approximation; Indeterminate forms and L'Hospital's rule.
Module 3: Sequences and series: (10 hours)
Limits of sequence of numbers, Calculation of limits, Infinite series; Tests for convergence; Power series, Taylor and Maclaurin series; Taylor theorem, convergence of Taylor series, error estimates.

Module 4: Multivariable Calculus (Differentiation): (8 hours)
Limit, continuity and partial derivatives, directional derivatives, gradient, total derivative; Tangent plane and normal line; Maxima, minima and saddle points; Method of Lagrange multipliers.

Module 5: Multivariable Calculus (Integration): (10 hours)
Multiple Integration: Double integrals (Cartesian), change of order of integration in double integrals, Change of variables (Cartesian to polar), Applications: areas and volumes, Center of mass and Gravity (constant and variable densities); Triple integrals (Cartesian), orthogonal curvilinear coordinates, Simple applications involving cubes, sphere and rectangular parallelepipeds; Scalar line integrals, vector line integrals, scalar surface integrals, vector surface integrals, Gradient, curl and divergence, Theorems of Green, Gauss and Stokes.

TEXTBOOKS/REFERENCES:

1. AICTE’s Prescribed Textbook: Mathematics-I (Calculus & Linear Algebra), Khanna Book Publishing Co.

Note: The modules have been prepared keeping the following from the Textbooks/References in mind:
(1) Module 1: The relevant sections from Chapters 2, 6 and 11 of [3].
(2) Module 2: Sections 3.1, 3.2, 3.3, 3.7 & 6.6 of [1].
(3) Module 3: Sections 8.1-8.6, 8.8-8.10 of [1].
(4) Module 4: Sections 12.1-12.5, 12.7-12.9 of [1].

Course outcomes: The objective of this course is to familiarize the prospective engineers with techniques in calculus, multivariate differentiation and integration. It aims to equip the students with standard concepts and tools at an intermediate to advanced level that will serve them well towards tackling more advanced level of mathematics and applications that they would find useful in their disciplines.
The students will learn

- To apply differential and integral calculus to notions of curvature and to improper integrals. Apart from some other applications they will have a basic understanding of Beta and Gamma functions.
- The fallouts of Rolle’s Theorem that is fundamental to application of analysis to Engineering problems.
- The tool of power series and Fourier series for learning advanced Engineering Mathematics.
- To deal with functions of several variables that are essential in most branches of engineering.
- To acquaint the student with mathematical tools needed in evaluating multiple integrals and their usage.

****

<table>
<thead>
<tr>
<th>Course Code</th>
<th>ES-01</th>
</tr>
</thead>
<tbody>
<tr>
<td>Course Title</td>
<td>Basic Electrical Engineering</td>
</tr>
<tr>
<td>Number of Credits</td>
<td>4 (L: 2, T: 1, P: 2)</td>
</tr>
<tr>
<td>Course Category</td>
<td>Engineering Science Courses</td>
</tr>
</tbody>
</table>

Course Objective: The objective of this Course is to provide the students with an introductory and broad treatment of the field of Electrical Engineering.

Course Contents:

Module I: D.C. Circuits covering, Ohm's Law and Kirchhoff’s Laws; Analysis of series, parallel and series-parallel circuits excited by independent voltage sources; Power and energy; Electromagnetism covering, Faradays Laws, Lenz's Law, Fleming's Rules, Statically and dynamically induced EMF; Concepts of self-inductance, mutual inductance and coefficient of coupling; Energy stored in magnetic fields;

Module II: Single Phase A.C. Circuits covering, Generation of sinusoidal voltage- definition of average value, root mean square value, form factor and peak factor of sinusoidal voltage and current and phasor representation of alternating quantities; Analysis with phasor diagrams of R, L, C, RL, RC and RLC circuits; Real power, reactive power, apparent power and power factor, series, parallel and series-parallel circuits; Three Phase A.C. Circuits covering, Necessity and Advantages of three phase systems, Generation of three phase power, definition of Phase sequence, balanced supply and balanced load; Relationship between line and phase values of balanced star and delta connections; Power in balanced three phase circuits, measurement of power by two wattmeter method;

Module III: Transformers covering, Principle of operation and construction of single phase transformers (core and shell types). EMF equation, losses, efficiency and voltage regulation; Synchronous Generators covering, Principle of operation; Types and constructional features; EMF equation;

Module IV: DC Machines covering, working principle of DC machine as a generator and a motor; Types and constructional features; EMF equation of generator, relation between EMF induced and
terminal voltage enumerating the brush drop and drop due to armature reaction; DC motor working principle; Back EMF and its significance, torque equation; Types of D.C. motors, characteristics and applications; Necessity of a starter for DC motor;

**Module V:** Three Phase Induction Motors covering; Concept of rotating magnetic field; Principle of operation, types and constructional features; Slip and its significance; Applications of squirrel cage and slip ring motors; Necessity of a starter, star-delta starter.

**Module VI:** Sources of Electrical Power covering, Introduction to Wind, Solar, Fuel cell, Tidal, Geothermal, Hydroelectric, Thermal-steam, diesel, gas, nuclear power plants; Concept of cogeneration, and distributed generation;

**TEXT/REFERENCES BOOKS:**
2. Ritu Sahdev (2022), Basic Electrical Engineering, Khanna Book Publishing.

**Alternative NPTEL/SWAYAM Course:**

<table>
<thead>
<tr>
<th>S. No.</th>
<th>NPTEL Course Name</th>
<th>Instructor</th>
<th>Host Institute</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>BASIC ELECTRIC CIRCUITS</td>
<td>PROF. ANKUSH SHARMA</td>
<td>IIT KANPUR</td>
</tr>
<tr>
<td>2</td>
<td>BASIC ELECTRICAL CIRCUITS</td>
<td>PROF. NAGENDRA KRISHNAPURA</td>
<td>IITM</td>
</tr>
<tr>
<td>3</td>
<td>FUNDAMENTALS OF ELECTRICAL ENGINEERING</td>
<td>PROF. DEBAPRIYA DAS</td>
<td>IIT KGP</td>
</tr>
</tbody>
</table>

**COURSE OUTCOMES:**
The students will learn:
1. To explain strong basics of Electrical Engineering and practical implementation of Electrical fundamentals.
2. To identify different applications of commonly used electrical machinery.

*****
| Course Code   | : | ES-02 |
|--------------|:|------|
| Course Title | : | Engineering Graphics & Design |
| Number of Credits | : | 3 (L: 1, T: 0, P: 4) |
| Course Category | : | Engineering Science Courses |

**COURSE OBJECTIVE(S):**
The objective of this Course is to provide the basic knowledge about Engineering Drawing. Detailed concepts are given in projections, technical drawing, dimensioning and specifications, so useful for a student in preparing for an engineering career.

**COURSE CONTENTS:**

**Traditional Engineering Graphics:** Principles of Engineering Graphics; Orthographic Projection; Descriptive Geometry; Drawing Principles; Isometric Projection; Surface Development; Perspective; Reading a Drawing; Sectional Views; Dimensioning & Tolerances; True Length, Angle; intersection, Shortest Distance.

**Computer Graphics:** Engineering Graphics Software: -Spatial Transformations; Orthographic Projections; Model Viewing; Co-ordinate Systems; Multi-view Projection; Exploded Assembly; Model Viewing; Animation; Spatial Manipulation; Surface Modelling; Solid Modelling; Introduction to Building Information Modelling (BIM).

*(Except the basic essential concepts, most of the teaching part can happen concurrently in the laboratory)*

**Module I: Introduction to Engineering Drawing**
Principles of Engineering Graphics and their significance, usage of Drawing instruments, lettering, Conic sections including the Rectangular Hyperbola (General method only); Cycloid, Epicycloid, Hypocycloid and Involute; Scales – Plain, Diagonal and Vernier Scales;

**Module II: Orthographic Projections**
Principles of Orthographic Projections-Conventions - Projections of Points and lines inclined to both planes; Projections of planes inclined Planes - Auxiliary Planes;

**Module III: Projections of Regular Solids**
Covering those inclined to both the Planes- Auxiliary Views; Draw simple annotation, dimensioning and scale. Floor plans that include: windows, doors, and fixtures such as WC, bath, sink, shower, etc.

**Module IV: Sections and Sectional Views of Right Angular Solids**
Prism, Cylinder, Pyramid, Cone – Auxiliary Views; Development of surfaces of Right Regular Solids - Prism, Pyramid, Cylinder and Cone; Draw the sectional orthographic views of geometrical solids, objects from industry and dwellings (foundation to slab only).
Module V: Isometric Projections
Principles of Isometric projection – Isometric Scale, Isometric Views, Conventions; Isometric Views of lines, Planes, Simple and compound Solids; Conversion of Isometric Views to Orthographic Views and Vice-versa, Conventions;

Module VI: Overview of Computer Graphics
Listing the computer technologies that impact on graphical communication, Demonstrating knowledge of the theory of CAD software [such as: The Menu System, Toolbars (Standard, Object Properties, Draw, Modify and Dimension), Drawing Area (Background, Crosshairs, Coordinate System), Dialog boxes and windows, Shortcut menus (Button Bars), The Command Line (where applicable), The Status Bar, Different methods of zoom as used in CAD, Select and erase objects.; Isometric Views of lines, Planes, Simple and compound Solids];

Module VII: Customisation & CAD Drawing
Consisting of set up of the drawing page and the printer, including scale settings, setting up of Modules and drawing limits; ISO and ANSI standards for coordinate dimensioning and tolerancing; Orthographic constraints, Snap to objects manually and automatically; Producing drawings by using various coordinate input entry methods to draw straight lines, Applying various ways of drawing circles;

Module VIII: Annotations, layering & other functions
Covering applying dimensions to objects, applying annotations to drawings; Setting up and use of Layers, layers to create drawings, Create, edit and use customized layers; Changing line lengths through modifying existing lines (extend/lengthen); Printing documents to paper using the print command; orthographic projection techniques; Drawing sectional views of composite right regular geometric solids and project the true shape of the sectioned surface; Drawing annotation, Computer-aided design (CAD) software modeling of parts and assemblies. Parametric and non-parametric solid, surface, and wireframe models. Part editing and two-dimensional documentation of models. Planar projection theory, including sketching of perspective, isometric, multiview, auxiliary, and section views. Spatial visualization exercises. Dimensioning guidelines, tolerancing techniques; dimensioning and scale multi views of dwelling;

Module IX: Demonstration of a simple team design project that illustrates
Geometry and topology of engineered components: creation of engineering models and their presentation in standard 2D blueprint form and as 3D wire-frame and shaded solids; meshed topologies for engineering analysis and tool-path generation for component manufacture; geometric dimensioning and tolerancing; Use of solid-modeling software for creating associative models at the component and assembly levels; floor plans that include: windows, doors, and fixtures such as WC, bath, sink, shower, etc. Applying colour coding according to building drawing practice; Drawing sectional elevation showing foundation to ceiling; Introduction to Building Information Modelling (BIM).

Text/Reference Books:

7. (Corresponding set of) CAD Software Theory and User Manuals.

Alternative NPTEL/SWAYAM Course:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>NPTEL Course Name</th>
<th>Instructor</th>
<th>Host Institute</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PROF. RAJARAM LAKKARAJU</td>
<td>IIT KGP</td>
<td>ENGINEERING DRAWING AND COMPUTER GRAPHICS</td>
</tr>
<tr>
<td>2</td>
<td>PROF. NIHAR RANJAN PATRA</td>
<td>IIT KANPUR</td>
<td>ENGINEERING GRAPHICS</td>
</tr>
</tbody>
</table>

Course Outcomes:

All phases of manufacturing or construction require the conversion of new ideas and design concepts into the basic line language of graphics. Therefore, there are many areas (civil, mechanical, electrical, architectural and industrial) in which the skills of the CAD technicians play major roles in the design and development of new products or construction. Students prepare for actual work situations through practical training in a new state-of-the-art computer designed CAD laboratory using engineering software. This course is designed to address:

- to prepare you to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability
- to prepare you to communicate effectively
- to prepare you to use the techniques, skills, and modern engineering tools necessary for engineering practice

The students will learn:

- To describe engineering design and its place in society.
- To discuss the visual aspects of engineering design.
- To use engineering graphics standards.
- To illustrate solid modelling.
- To use computer-aided geometric design.
- To design creating working drawings.
- To inspect engineering communication.

*****
Course Objective:

- To provide learning environment to practice listening, speaking, reading and writing skills.
- To assist the students to carry on the tasks and activities through guided instructions and materials.
- To effectively integrate English language learning with employability skills and training.
- To provide hands-on experience through case-studies, mini-projects, group and individual presentations.

Course Content:

**Module I: Vocabulary Building**
1.1. The concept of Word Formation
1.2. Root words from foreign languages and their use in English
1.3. Acquaintance with prefixes and suffixes from foreign languages in English to form derivatives.
1.4. Synonyms, antonyms, and standard abbreviations.

**Module II: Basic Writing Skills**
1.1. Sentence Structures
1.2. Use of phrases and clauses in sentences
1.3. Importance of proper punctuation
1.4. Creating coherence
1.5. Organizing principles of paragraphs in documents
1.6. Techniques for writing precisely

**Module III: Identifying Common Errors in Writing**
1.1. Subject-verb agreement
1.2. Noun-pronoun agreement
1.3. Misplaced modifiers
1.4. Articles
1.5. Prepositions
1.6. Redundancies
1.7. Clichés

**Module IV: Nature and Style of sensible Writing**
1.1. Describing
1.2. Defining
1.3. Classifying
1.4. Providing examples or evidence
1.5. Writing introduction and conclusion
Module V: Writing Practices
1.1. Comprehension
1.2. Précis Writing
1.3. Essay Writing

Module VI: Oral Communication
(This Module involves interactive practice sessions in Language Lab)
- Listening Comprehension
- Pronunciation, Intonation, Stress and Rhythm
- Common Everyday Situations: Conversations and Dialogues
- Communication at Workplace
- Interviews
- Formal Presentations

Text/Reference Books:

Alternative NPTEL/SWAYAM Course:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>NPTEL Course Name</th>
<th>Instructor</th>
<th>Host Institute</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>ENGLISH LANGUAGE FOR COMPETITIVE EXAMS</td>
<td>PROF. AYSHA IQBAL</td>
<td>IIT MADRAS</td>
</tr>
<tr>
<td>2</td>
<td>TECHNICAL ENGLISH FOR ENGINEERS</td>
<td>PROF. AYSHA IQBAL</td>
<td>IITM</td>
</tr>
</tbody>
</table>

Course Outcomes: The student will acquire basic proficiency in English including reading and listening comprehension, writing and speaking skills.

*****
Course Code : ES-03
Course Title : Design Thinking
Number of Credits : 1 (L: 0, T: 0, P: 2)
Course Category : Engineering Science Courses

COURSE OBJECTIVE(S):
The objective of this Course is to provide the new ways of creative thinking and Learn the innovation cycle of Design Thinking process for developing innovative products which useful for a student in preparing for an engineering career.

COURSE CONTENTS:
Unit 1: An Insight to Learning
Understanding the Learning Process, Kolb’s Learning Styles, Assessing and Interpreting

Unit 2: Remembering Memory
Understanding the Memory process, Problems in retention, Memory enhancement techniques

Unit 3: Emotions: Experience & Expression
Understanding Emotions: Experience & Expression, Assessing Empathy, Application with Peers

Unit 4: Basics of Design Thinking
Definition of Design Thinking, Need for Design Thinking, Objective of Design Thinking, Concepts & Brainstorming, Stages of Design Thinking Process (explain with examples) – Empathize, Define, Ideate, Prototype, Test

Unit 5: Being Ingenious & Fixing Problem
Understanding Creative thinking process, Understanding Problem Solving, Testing Creative Problem Solving

Unit 6: Process of Product Design

Unit 7: Prototyping & Testing
What is Prototype? Why Prototype? Rapid Prototype Development process, Testing, Sample Example, Test Group Marketing

Unit 8: Celebrating the Difference
Understanding Individual differences & Uniqueness, Group Discussion and Activities to encourage the understanding, acceptance and appreciation of Individual differences

Unit 9: Design Thinking & Customer Centricity
Practical Examples of Customer Challenges, Use of Design Thinking to Enhance Customer Experience, Parameters of Product experience, Alignment of Customer Expectations with Product Design
Unit 10: Feedback, Re-Design & Re-Create
Feedback loop, Focus on User Experience, Address “ergonomic challenges, User focused design, rapid prototyping & testing, final product, Final Presentation – “Solving Practical Engineering Problem through Innovative Product Design & Creative Solution”.

Course Outcomes (CO):
Student will able to
1. Compare and classify the various learning styles and memory techniques and Apply them in their engineering education
2. Analyze emotional experience and Inspect emotional expressions to better understand users while designing innovative products
3. Develop new ways of creative thinking and Learn the innovation cycle of Design Thinking process for developing innovative products
4. Propose real-time innovative engineering product designs and Choose appropriate frameworks, strategies, techniques during prototype development
5. Perceive individual differences and its impact on everyday decisions and further Create a better customer experience

Text/Reference Books:
1. E Balaguruswamy (2022), Developing Thinking Skills (The way to Success), Khanna Book Publishing Company.
Course Code : AU-01
Course Title : IDEA Lab Workshop
Number of Credits : 0 (L: 2, T: 0, P: 4)
Course Category : AU-101
Prerequisites : None

Course Objectives:
1. To learn all the skills associated with the tools and inventory associated with the IDEA Lab.
2. Learn useful mechanical and electronic fabrication processes.
3. Learn necessary skills to build useful and standalone system/ project with enclosures.
4. Learn necessary skills to create print and electronic documentation for the system/project

Course Contents:

<table>
<thead>
<tr>
<th>Unit #</th>
<th>Topics</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Electronic component familiarization, Understanding electronic system design flow. Schematic design and PCB layout and Gerber creation using EagleCAD. Documentation using Doxygen, Google Docs, Overleaf. Version control tools - GIT and GitHub. Basic 2D and 3D designing using CAD tools such as FreeCAD, Sketchup, Prusa Slicer, FlatCAM, Inkspace, OpenBSP and VeriCUT.</td>
<td>Introduction to basic hand tools - Tape measure, combination square, Vernier calliper, hammers, fasteners, wrenches, pliers, saws, tube cutter, chisels, vice and clamps, tapping and threading. Adhesives Introduction to Power tools: Power saws, band saw, jigsaw, angle grinder, belt sander, bench grinder, rotary tools. Various types of drill bits,</td>
</tr>
<tr>
<td>2</td>
<td>Familiarization and use of basic measurement instruments - DSO including various triggering modes, DSO probes, DMM, LCR bridge, Signal and function generator. Logic analyzer and MSO. Bench power supply (with 4-wire output) Circuit prototyping using (a) breadboard, (b) Zero PCB (c) ‘Manhattan’ style and (d) custom PCB. Single, double and multilayer PCBs. Single and double-sided PCB prototype fabrication in the lab. Soldering using soldering iron/station. Soldering using a temperature controlled reflow oven. Automated circuit assembly</td>
<td>Mechanical cutting processes - 3-axis CNC routing, basic turning, milling, drilling and grinding operations, Laser cutting, Laser engraving etc. Basic welding and brazing and other joining techniques for assembly. Concept of Lab aboard a Box.</td>
</tr>
</tbody>
</table>

3D printing and prototyping technology – 3D printing using FDM, SLS and SLA. Basics of 3D scanning, point cloud data generation for reverse engineering.

Prototyping using subtractive cutting processes. 2D and 3D Structures for prototype building using Laser cutter and CNC routers.

Basics of IPR and patents; Accessing and utilizing patent information in IDEA Lab.

4. Discussion and implementation of a mini project.

5. Documentation of the mini project (Report and video).

### Laboratory Activities:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>List of Lab activities and experiments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Schematic and PCB layout design of a suitable circuit, fabrication and testing of the circuit.</td>
</tr>
<tr>
<td>2.</td>
<td>Machining of 3D geometry on soft material such as soft wood or modelling wax.</td>
</tr>
<tr>
<td>3.</td>
<td>3D scanning of computer mouse geometry surface. 3D printing of scanned geometry using FDM or SLA printer.</td>
</tr>
<tr>
<td>4.</td>
<td>2D profile cutting of press fit box/casing in acrylic (3 or 6 mm thickness)/cardboard, MDF (2 mm) board using laser cutter &amp; engraver.</td>
</tr>
<tr>
<td>5.</td>
<td>2D profile cutting on plywood /MDF (6-12 mm) for press fit designs.</td>
</tr>
<tr>
<td>6.</td>
<td>Familiarity and use of welding equipment.</td>
</tr>
<tr>
<td>7.</td>
<td>Familiarity and use of normal and wood lathe.</td>
</tr>
</tbody>
</table>
8. Embedded programming using Arduino and/or Raspberry Pi.

9. Design and implementation of a capstone project involving embedded hardware, software and machined or 3D printed enclosure.

Reference Books:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Title</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td><strong>AICTE’s Prescribed Textbook: Workshop / Manufacturing Practices (with Lab Manual), Khanna Book Publishing.</strong></td>
</tr>
<tr>
<td>No.</td>
<td>Reference Details</td>
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</tbody>
</table>

*****
SEMESTER – II
SEMESTER II

<table>
<thead>
<tr>
<th>Course Code</th>
<th>: BS-03</th>
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</thead>
<tbody>
<tr>
<td>Course Title</td>
<td>Chemistry- I</td>
</tr>
<tr>
<td>Number of Credits</td>
<td>4 (L: 3, T: 0, P: 2)</td>
</tr>
<tr>
<td>Course Category</td>
<td>Basic Science Course</td>
</tr>
</tbody>
</table>

Course Objective:

The objective of the Chemistry I is to acquaint the students with the basic phenomenon/concepts of chemistry, the student faces during course of their study in the industry and Engineering field. The student with the knowledge of the basic chemistry, will understand and explain scientifically the various chemistry related problems in the industry/engineering field. The student will able to understand the new developments and breakthroughs efficiently in engineering and technology. The introduction of the latest (R&D oriented) topics will make the engineering student upgraded with the new technologies.

Course Content:

**Module I: Atomic and Molecular Structure**
Schrodinger equation. Particle in a box solutions and their applications for conjugated molecules and nanoparticles. Forms of the hydrogen atom wave functions and the plots of these functions to explore their spatial variations. Molecular orbitals of diatomic molecules and plots of the multicentre orbitals. Equations for atomic and molecular orbitals. Energy level diagrams of diatomic. Pi-molecular orbitals of butadiene and benzene and aromaticity. Crystal field theory and the energy level diagrams for transition metal ions and their magnetic properties. Band structure of solids and the role of doping on band structures.

**Module II: Spectroscopic techniques and applications**

**Module III: Intermolecular forces and potential energy surfaces**
Ionic, dipolar and van Der Waals interactions. Equations of state of real gases and critical phenomena. Potential energy surfaces of H3, H2F and HCN and trajectories on these surfaces.

**Module IV: Use of free energy in chemical equilibria (6 lectures)**
Module V: Periodic properties
Effective nuclear charge, penetration of orbitals, variations of s, p, d and f orbital energies of atoms in the periodic table, electronic configurations, atomic and ionic sizes, ionization energies, electron affinity and electronegativity, polarizability, oxidation states, coordination numbers and geometries, hard soft acids and bases, molecular geometries.

Module VI: Stereochemistry
Representations of 3 dimensional structures, structural isomers and stereoisomers, configurations and symmetry and chirality, enantiomers, diastereomers, optical activity, absolute configurations and conformational analysis. Isomerism in transitional metal compounds.

Module VII: Organic reactions and synthesis of a drug molecule
Introduction to reactions involving substitution, addition, elimination, oxidation, reduction, cyclization and ring openings. Synthesis of a commonly used drug molecule.

LABORATORY
Choice of 10-12 experiments from the following:
1. Determination of surface tension and viscosity.
2. Thin layer chromatography.
3. Ion exchange column for removal of hardness of water.
4. Determination of chloride content of water.
5. Colligative properties using freezing point depression.
6. Determination of the rate constant of a reaction.
7. Determination of cell constant and conductance of solutions.
10. Saponification/acid value of an oil.
11. Chemical analysis of a salt.
12. Lattice structures and packing of spheres.
15. Determination of the partition coefficient of a substance between two immiscible liquids.
16. Adsorption of acetic acid by charcoal.
17. Use of the capillary viscosimeters to demonstrate the isoelectric point as the pH of minimum viscosity for gelatin sols and/or coagulation of the white part of egg.

Text/Reference Books:
3. University chemistry, by B. H. Mahan
5. Fundamentals of Molecular Spectroscopy, by C. N. Banwell
6. Engineering Chemistry (NPTEL Web-book), by B. L. Tembe, Kamaluddin and M. S. Krishnan
7. Physical Chemistry, by P. W. Atkins
   http://bcs.whfreeman.com/vollhardtschore5e/default.asp
Alternative NPTEL/SWAYAM Course:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>NPTEL Course Name</th>
<th>Instructor</th>
<th>Host Institute</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>CHEMISTRY - I</td>
<td>PROF. MANGALA SUNDER</td>
<td>IITM</td>
</tr>
<tr>
<td></td>
<td></td>
<td>KRISHNAN</td>
<td></td>
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</tbody>
</table>

EXPERIMENTS THAT MAY BE PERFORMED THROUGH VIRTUAL LABS:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Experiment Name</th>
<th>Experiment Link(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>4</td>
<td>Colligative properties using freezing point depression.</td>
<td><a href="http://pcv-au.vlabs.ac.in/physical-chemistry/Cryoscopy/">http://pcv-au.vlabs.ac.in/physical-chemistry/Cryoscopy/</a></td>
</tr>
<tr>
<td>5</td>
<td>Determination of the rate constant of a reaction.</td>
<td><a href="http://pcv-au.vlabs.ac.in/physical-chemistry/EMF_Measurement/">http://pcv-au.vlabs.ac.in/physical-chemistry/EMF_Measurement/</a></td>
</tr>
<tr>
<td>8</td>
<td>Saponification/acid value of an oil.</td>
<td><a href="http://biotech01.vlabs.ac.in/biochemistry/Estimation_of_Saponification_Value_of_Fats_or_Oils/">http://biotech01.vlabs.ac.in/biochemistry/Estimation_of_Saponification_Value_of_Fats_or_Oils/</a></td>
</tr>
<tr>
<td>9</td>
<td>Lattice structures and packing of spheres.</td>
<td><a href="https://vlab.amrita.edu/?sub=1&amp;brch=282&amp;sim=370&amp;cnt=1">https://vlab.amrita.edu/?sub=1&amp;brch=282&amp;sim=370&amp;cnt=1</a></td>
</tr>
</tbody>
</table>
**Course Outcomes:** The concepts developed in this course will aid in quantification of several concepts in chemistry that have been introduced at the 10+2 levels in schools. Technology is being increasingly based on the electronic, atomic and molecular level modifications. Quantum theory is more than 100 years old and to understand phenomena at nanometre levels, one has to base the description of all chemical processes at molecular levels. The course will enable the students:

- To analyse microscopic chemistry in terms of atomic and molecular orbitals and intermolecular forces.
- To rationalise bulk properties and processes using thermodynamic considerations.
- To distinguish the ranges of the electromagnetic spectrum used for exciting different molecular energy levels in various spectroscopic techniques.
- To rationalise periodic properties such as ionization potential, electronegativity, oxidation states and electronegativity.
- To list major chemical reactions that are used in the synthesis of molecules.

**Laboratory Outcomes:** The chemistry laboratory course will consist of experiments illustrating the principles of chemistry relevant to the study of science and engineering. The students will learn:

- To estimate rate constants of reactions from concentration of reactants/products as a function of time.
- To measure molecular/system properties such as surface tension, viscosity, conductance of solutions, redox potentials, chloride content of water, etc.
- To synthesize a small drug molecule and analyze a salt sample.

<table>
<thead>
<tr>
<th>Course Code</th>
<th>BS-04</th>
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</thead>
<tbody>
<tr>
<td>Course Title</td>
<td>Mathematics- II</td>
</tr>
<tr>
<td>Number of Credits</td>
<td>4 (L: 3, T: 1, P: 0)</td>
</tr>
<tr>
<td>Course Category</td>
<td>Basic Science Course</td>
</tr>
</tbody>
</table>

**Course Objective:** Mathematics fundamental necessary to formulate, solve and analyze engineering problems.

**Course Content:**

**Module 1: Matrices** (10 hours)
Linear Systems of Equations; Linear Independence; Rank of a Matrix; Determinant, Inverse of a matrix, rank-nullity theorem; System of linear equations; Symmetric, skew-symmetric and orthogonal matrices; Determinants; Eigenvalues and eigenvectors; Orthogonal transformation; Diagonalization of matrices; Cayley-Hamilton Theorem.

**Module 2: First order ordinary differential equations:** (6 hours)
Exact, linear and Bernoulli’s equations. Equations not of first degree: equations solvable for p, equations solvable for y, equations solvable for x and Clairaut’s type.
Module 3: Ordinary differential equations of higher orders: (8 hours)
Second order linear differential equations with variable coefficients: Euler-Cauchy equations, solution by variation of parameters; Power series solutions: Legendre’s equations and Legendre polynomials, Frobenius method, Bessel’s equation and Bessel’s functions of the first kind and their properties.

Module 4: Complex Variable – Differentiation: (8 hours):
Differentiation, Cauchy-Riemann equations, analytic functions, harmonic functions, finding harmonic conjugate; elementary analytic functions (exponential, trigonometric, logarithm) and their properties; Conformal mappings, Mobius transformations and their properties.

Module 5: Complex Variable – Integration: (8 hours):
Contour integrals, Cauchy-Goursat theorem (without proof), Cauchy Integral formula (without proof), Liouville’s theorem and Maximum-Modulus theorem (without proof); Taylor’s series, zeros of analytic functions, singularities, Laurent’s series; Residues, Cauchy Residue theorem (without proof), Evaluation of definite integral involving sine and cosine, Evaluation of certain improper integrals using the Bromwich contour.

TEXTBOOKS/REFERENCES:

1. **AICTE’s Prescribed Textbook: Mathematics-II (Calculus, Ordinary Differential Equations and Complex Variable), Khanna Book Publishing Co.**

**Note:** The modules have been prepared keeping the following from the Textbooks/References in mind:
(1) Module 1: Sections 7.3-7.5, 7.7, 7.8, 8.1-8.4 of [1].
(2) Module 2: Sections 1.4, 1.5 of [1]; Section 5.1 of [2].
(3) Module 3: Sections 2.5, 2.6, 2.10, 5.1, 5.3, 5.4, 5.5 of [1].
**COURSE OUTCOMES:** The objective of this course is to familiarize the prospective engineers with techniques in matrices, ordinary differential equations and complex variables. It aims to equip the students to deal with advanced level of mathematics and applications that would be essential for their disciplines.

The students will learn:
- The essential tool of matrices and linear algebra in a comprehensive manner.
- The effective mathematical tools for the solutions of differential equations that model physical processes.
- The tools of differentiation and integration of functions of a complex variable that are used in various techniques dealing engineering problems.

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<tr>
<th>Course Code</th>
<th>:</th>
<th>ES-04</th>
</tr>
</thead>
<tbody>
<tr>
<td>Course Title</td>
<td>:</td>
<td>Programming for Problem Solving</td>
</tr>
<tr>
<td>Number of Credits</td>
<td>:</td>
<td>4 (L: 2, T: 0, P: 4)</td>
</tr>
<tr>
<td>Course Category</td>
<td>:</td>
<td>Engineering Science Courses</td>
</tr>
</tbody>
</table>

**Course Objectives:**
1. To learn the fundamentals of computers.
2. To understand the various steps in program development.
3. To learn the syntax and semantics of C programming language.
4. To learn the usage of structured programming approach in solving problems.
5. To understated and formulate algorithm for programming script
6. To analyze the output based on the given input variables

**Course Contents:**

**Module I:** Introduction to Programming; Introduction to components of a computer system (disks, memory, processor, where a program is stored and executed, operating system, compilers etc.)

Idea of Algorithm: steps to solve logical and numerical problems. Representation of Algorithm: Flowchart/Pseudocode with examples.

From algorithms to programs; source code, variables (with data types) variables and memory locations, Syntax and Logical Errors in compilation, object and executable code.

**Module II:** Arithmetic expressions and precedence.

**Module III:** Conditional Branching and Loops. Writing and evaluation of conditionals and consequent branching. Iteration and loops.

**Module IV:** Arrays, Arrays (1-D, 2-D), Character arrays and Strings
Module V: Basic Algorithms, Searching, Basic Sorting Algorithms (Bubble, Insertion and Selection), Finding roots of equations, notion of order of complexity through example programs (no formal definition required)

Module VI: Function, Functions (including using built in libraries), Parameter passing in functions, call by value, Passing arrays to functions: idea of call by reference

Module VII: Recursion, Recursion as a different way of solving problems. Example programs, such as Finding Factorial, Fibonacci series, Ackerman function etc. Quick sort or Merge sort.

Module VIII: Structures, Defining structures and Array of Structures

Module IX: Pointers, Idea of pointers, Defining pointers, Use of Pointers in self-referential structures, notion of linked list (no implementation)

Module X: File handling (only if time is available, otherwise should be done as part of the lab).

PRACTICALS:
1. Familiarization with programming environment
2. Simple computational problems using arithmetic expressions
3. Problems involving if-then-else structures
4. Iterative problems e.g., sum of series
5. 1D Array manipulation
6. Matrix problems, String operations
7. Simple functions
8. Programming for solving Numerical methods problems
9. Recursive functions
10. Pointers and structures
11. File operations

TEXT/REFERENCE BOOKS:


Alternative NPTEL/SWAYAM Course:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>NPTEL Course Name</th>
<th>Instructor</th>
<th>Host Institute</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>INTRODUCTION TO PROGRAMMING IN C</td>
<td>PROF. SATYADEV NANDAKUMAR</td>
<td>IITK</td>
</tr>
<tr>
<td>2</td>
<td>PROBLEM SOLVING THROUGH PROGRAMMING IN C</td>
<td>PROF. ANUPAM BASU</td>
<td>IIT KGP</td>
</tr>
</tbody>
</table>
EXPERIMENTS THAT MAY BE PERFORMED THROUGH VIRTUAL LABS:

<table>
<thead>
<tr>
<th>S. No.</th>
<th>Experiment Name</th>
<th>Experiment Link(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>1D Array manipulation.</td>
<td><a href="http://cse02-iiith.vlabs.ac.in/exp4/index.html">http://cse02-iiith.vlabs.ac.in/exp4/index.html</a></td>
</tr>
<tr>
<td>5</td>
<td>Simple functions.</td>
<td><a href="http://cse02-iiith.vlabs.ac.in/exp2/index.html">http://cse02-iiith.vlabs.ac.in/exp2/index.html</a></td>
</tr>
</tbody>
</table>

COURSE OUTCOMES: The student will learn following through lectures:

- To formulate simple algorithms for arithmetic and logical problems.
- To translate the algorithms to programs (in C language).
- To test and execute the programs and correct syntax and logical errors.
- To implement conditional branching, iteration and recursion.
- To decompose a problem into functions and synthesize a complete program using divide and conquer approach.
- To use arrays, pointers and structures to formulate algorithms and programs.
- To apply programming to solve matrix addition and multiplication problems and searching and sorting problems.
- To apply programming to solve simple numerical method problems, namely root finding of function, differentiation of function and simple integration.
The student will learn following through Practicals:

- To formulate the algorithms for simple problems.
- To translate given algorithms to a working and correct program.
- To be able to correct syntax errors as reported by the compilers.
- To be able to identify and correct logical errors encountered at run time.
- To be able to write iterative as well as recursive programs.
- To be able to represent data in arrays, strings and structures and manipulate them through a program.
- To be able to declare pointers of different types and use them in defining self-referential structures.
- To be able to create, read and write to and from simple text files.

*****

| BS-05 | Biology (Biology for Engineers) | 2L:1T:0P | 3 credits |

Module 1. Introduction

**Purpose:** To convey that Biology is as important a scientific discipline as Mathematics, Physics and Chemistry. Bring out the fundamental differences between science and engineering by drawing a comparison between eye and camera, Bird flying and aircraft. Mention the most exciting aspect of biology as an independent scientific discipline. Why we need to study biology? Discuss how biological observations of 18th Century that lead to major discoveries. Examples from Brownian motion and the origin of thermodynamics by referring to the original observation of Robert Brown and Julius Mayor. These examples will highlight the fundamental importance of observations in any scientific inquiry.

Module 2. Classification

**Purpose:** To convey that classification per se is not what biology is all about. The underlying criterion, such as morphological, biochemical or ecological be highlighted. Hierarchy of life forms at phenomenological level. A common thread weaves this hierarchy Classification. Discuss classification based on (a) cellularity- Unicellular or multicellular (b) ultrastructure- prokaryotes or eucaryotes. (c) energy and Carbon utilization -Autotrophs, heterotrophs, lithotropes (d) Ammonia excretion – aminotelic, uricotelie, ureotelic (e) Habitat- aquatic or terrestrial (e) Molecular taxonomy- three major kingdoms of life. A given organism can come under different category based on classification. Model organisms for the study of biology come from different groups. E.coli, S.cerevisiae, D. Melanogaster, C. elegance, A. Thaliana, M. musculus

Module 3 -Genetics

**Purpose:** To convey that “Genetics is to biology what Newton’s laws are to Physical Sciences” Mendel’s laws, Concept of segregation and independent assortment. Concept of allele. Gene mapping, Gene interaction, Epistasis. Meiosis and Mitosis be taught as a part of genetics. Emphasis to be give not to the mechanics of cell division nor the phases but how genetic material passes from parent to offspring. Concepts of recessiveness and dominance. Concept of mapping of phenotype to genes. Discuss about the single gene disorders in humans. Discuss
the concept of complementation using human genetics.

**Module 4.** Biomolecules

**Purpose:** To convey that all forms of life has the same building blocks and yet the manifestations are as diverse as one can imagine. Molecules of life. In this context discuss monomeric units and polymeric structures. Discuss about sugars, starch and cellulose. Amino acids and proteins. Nucleotides and DNA/RNA. Two carbon units and lipids.

**Module 5.** Enzymes

**Purpose:** To convey that without catalysis life would not have existed on earth. Enzymology: How to monitor enzyme catalyzed reactions. How does an enzyme catalyze reactions. Enzyme classification. Mechanism of enzyme action. Discuss at least two examples. Enzyme kinetics and kinetic parameters. Why should we know these parameters to understand biology? RNA catalysis.

**Module 6.** Information Transfer

**Purpose:** The molecular basis of coding and decoding genetic information is universal. Molecular basis of information transfer. DNA as a genetic material. Hierarchy of DNA structure- from single stranded to double helix to nucleosomes. Concept of genetic code. Universality and degeneracy of genetic code. Define gene in terms of complementation and recombination. DICOM Image formats, The DNA Technology (Use and Application) Regulation Bill, 2019

**Module 7.** Macromolecular analysis

**Purpose:** How to analyses biological processes at the reductionistic level. Proteins- structure and function. Hierarch in protein structure. Primary secondary, tertiary and quaternary structure. Proteins as enzymes, transporters, receptors and structural elements.

**Module 8.** Metabolism

**Purpose:** The fundamental principles of energy transactions are the same in physical and biological world. Thermodynamics as applied to biological systems. Exothermic and endothermic versus endergonic and exergonic reactions. Concept of Keq and its relation to standard free energy. Spontaneity. ATP as an energy currency. This should include the breakdown of glucose to CO2 + H2O (Glycolysis and Krebs cycle) and synthesis of glucose from CO2 and H2O (Photosynthesis). Energy yielding and energy consuming reactions. Concept of Energy charge.

**Module 9.** Microbiology

References:

2) Biology: A global approach: Campbell, N. A.; Reece, J. B.; Urry, Lisa; Cain, M., L.; Wasserman, S. A.; Minorsky, P. V.; Jackson, R. B. Pearson Education Ltd
3) Outlines of Biochemistry, Conn, E.E; Stumpf, P.K; Bruening, G; Doi, R.H., John Wiley and Sons
5) Molecular Genetics (Second edition), Stent, G. S.; and Calender, R.W.H. Freeman and company, Distributed by Satish Kumar Jain for CBS Publisher

Course Outcomes

After studying the course, the student will be able to:
- Describe how biological observations of 18th Century that lead to major discoveries.
- Convey that classification per se is not what biology is all about but highlight the underlying criteria, such as morphological, biochemical and ecological
- Highlight the concepts of recessiveness and dominance during the passage of genetic material from parent to offspring
- Convey that all forms of life have the same building blocks and yet the manifestations are as diverse as one can imagine
- Classify enzymes and distinguish between different mechanisms of enzyme action.
- Identify DNA as a genetic material in the molecular basis of information transfer.
- Analyse biological processes at the reductionistic level
- Apply thermodynamic principles to biological systems.
- Identify and classify microorganisms

*****
Course Code : ES-05
Course Title : Digital Fabrication / Workshop/Manufacturing Practices
Number of Credits : 2 (L: 0, T: 0, P: 4)
Course Category : Engineering Science Courses

<table>
<thead>
<tr>
<th>Course Code</th>
<th>Digital Fabrication</th>
<th>0L:0T:4P</th>
<th>2 credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>ES- 05</td>
<td>Digital Fabrication</td>
<td></td>
<td>2 credits</td>
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</tbody>
</table>

Course Objective:
The course is designed to impart knowledge and skills related to 3D printing technologies, selection of material and equipment and develop a product using this technique in Industry 4.0 environment.

Course Content:

1. **3D Printing (Additive Manufacturing)**


2. **CAD for Additive Manufacturing**

   CAD Data formats, Data translation, Data loss, STL format.

3. **Additive Manufacturing Techniques**

   3.1 Stereo- Lithography, LOM, FDM, SLS, SLM, Binder Jet technology.
   3.2 Process, Process parameter, Process Selection for various applications.
   3.3 Additive Manufacturing Application Domains: Aerospace, Electronics, Health Care, Defence, Automotive, Construction, Food Processing, Machine Tools

4. **Materials**

   4.1 Polymers, Metals, Non-Metals, Ceramics
   4.2 Various forms of raw material- Liquid, Solid, Wire, Powder; Powder Preparation and their desired properties, Polymers and their properties.
   4.3 Support Materials
5. Additive Manufacturing Equipment
   5.1 Process Equipment- Design and process parameters
   5.2 Governing Bonding Mechanism
   5.3 Common faults and troubleshooting
   5.4 Process Design

6. Post Processing: Requirement and Techniques

7. Product Quality
   7.1 Inspection and testing
   7.2 Defects and their causes

LIST OF PRACTICALS

1. 3D Modelling of a single component.
2. Assembly of CAD modelled Components
3. Exercise on CAD Data Exchange.
6. Printing of identified product on an available AM machine.
7. Post processing of additively manufactured product.
8. Inspection and defect analysis of the additively manufactured product.
9. Comparison of Additively manufactured product with conventional manufactured counterpart.

Text/Reference Books:

Course Outcomes:

After completion of this course, the students will be able to:
1. Develop CAD models for 3D printing.
2. Import and Export CAD data and generate .stl file.
3. Select a specific material for the given application.
4. Select a 3D printing process for an application.
5. Produce a product using 3D Printing or Additive Manufacturing (AM).

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<table>
<thead>
<tr>
<th></th>
<th>Workshop/Manufacturing Practices</th>
<th>0L:0T:4P</th>
<th>2 credits</th>
</tr>
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<tr>
<td>ES-05</td>
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</table>

Course Objective:

1. To provide exposure to the students with hands on experience on various basic engineering practices in Civil, Mechanical, Electrical and Electronics Engineering.
2. To have a study and hands-on-exercise on plumbing and carpentry components.
3. To have a practice on gas welding, foundry operations and fitting
4. To have a study on measurement of electrical quantities, energy and resistance to earth.
5. To have a practice on soldering.

Course Content:

Module I: Manufacturing Methods- casting, forming, machining, joining, advanced manufacturing methods.
Module II: CNC machining, Additive manufacturing.
Module III: Fitting operations & power tools.
Module IV: Electrical & Electronics.
Module V: Carpentry.
Module VI: Plastic moulding, glass cutting.
Module VII: Metal casting.
Module VIII: Welding (arc welding & gas welding), brazing.

Practicals:

1. Machine shop
2. Fitting shop
3. Carpentry
4. Electrical & Electronics
5. Welding shop (Arc welding + Gas welding)
6. Casting
7. Smithy
8. Plastic moulding & Glass Cutting
Examinations could involve the actual fabrication of simple components, utilizing one or more of the techniques covered above.

Suggested Text/Reference Books:

EXPERIMENTS THAT MAY BE PERFORMED THROUGH VIRTUAL LABS:

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<tr>
<th>S. No.</th>
<th>Experiment Name</th>
<th>Experiment Link(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>Casting</td>
<td><a href="http://fab-coep.vlabs.ac.in/exp7/Theory.html?domain=Mechanical%20Engineering&amp;lab=Welcome%20to%20FAB%20laboratory">http://fab-coep.vlabs.ac.in/exp7/Theory.html?domain=Mechanical%20Engineering&amp;lab=Welcome%20to%20FAB%20laboratory</a></td>
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Course Outcomes: Upon completion of this course, the students will gain knowledge of the different manufacturing processes which are commonly employed in the industry, to fabricate components using different materials.

Laboratory Outcomes:
Upon completion of this laboratory course, students will be able:
- To fabricate components with their own hands.
- To relate practical knowledge of the dimensional accuracies and dimensional tolerances possible with different manufacturing processes.
- To design small devices of their interest by assembling different components.
Pre-requisites: None. Universal Human Values 1 (Desirable)

1-COURSES ON HUMAN VALUES

During the Induction Program, students would get an initial exposure to human values through Universal Human Values-I. This exposure is to be augmented by this compulsory full semester foundation course.

Objectives of UHV-II Course

This introductory course input is intended:

1. To help the students appreciate the essential complementarily between 'VALUES' and 'SKILLS' to ensure sustained happiness and prosperity which are the core aspirations of all human beings.
2. To facilitate the development of a Holistic perspective among students towards life and profession as well as towards happiness and prosperity based on a correct understanding of the Human reality and the rest of existence. Such a holistic perspective forms the basis of Universal Human Values and movement towards value-based living in a natural way.
3. To highlight plausible implications of such a Holistic understanding in terms of ethical human conduct, trustful and mutually fulfilling human behavior and mutually enriching interaction with Nature.

Thus, this course is intended to provide a much-needed orientational input in value education to the young enquiring minds.

Salient Features of the Course

The salient features of this course are:

1. It presents a universal approach to value education by developing the right understanding of reality (i.e. a worldview of the reality “as it is”) through the process of self-exploration.
2. The whole course is presented in the form of a dialogue whereby a set of proposals about various aspects of the reality are presented and the students are encouraged to self-explore the proposals by verifying them on the basis of their natural acceptance within oneself and validate experientially in living.
3. The prime focus throughout the course is toward affecting a qualitative transformation in the life of the student rather than just a transfer of information.
4. While introducing the holistic worldview and its implications, a critical appraisal of the prevailing notions is also made to enable the students discern the difference on their own right.

Course Methodology

1. The methodology of this course is explorational and thus universally adaptable. It involves a systematic and rational study of the human being vis-à-vis the rest of existence.
2. The course is in the form of 28 lectures (discussions) and 14 practice sessions.
3. It is free from any dogma or value prescriptions.
4. It is a process of self-investigation and self-exploration, and not of giving sermons. Whatever is found as truth or reality is stated as a proposal and the students are facilitated to verify it in their own right, based on their Natural Acceptance and subsequent Experiential Validation – the whole existence is the lab and every activity is a source of reflection.

5. This process of self-exploration takes the form of a dialogue between the teacher and the students to begin with, and then to continue within the student in every activity, leading to continuous self-evolution.

6. This self-exploration also enables them to critically evaluate their pre-conditionings and present beliefs.

2-COURSE TOPICS

The course has 28 lectures and 14 tutorials in 5 modules. The lectures and tutorials are of 01-hour duration. Tutorial sessions are to be used to explore and practice what has been proposed during the lecture sessions.

The Teacher’s Manual provides the outline for lectures as well as practice sessions. The teacher is expected to present the issues to be discussed as propositions and encourage the students to have a dialogue.

The syllabus for the lectures and practice sessions is given below:

Module 1 – Introduction to Value Education (6 lectures and 3 tutorials for practice session)

Lecture 1: Right Understanding, Relationship and Physical Facility (Holistic Development and the Role of Education)
Lecture 2: Understanding Value Education
Tutorial 1: Practice Session PS1 Sharing about Oneself
Lecture 3: Self-exploration as the Process for Value Education
Lecture 4: Continuous Happiness and Prosperity – the Basic Human Aspirations
Tutorial 2: Practice Session PS2 Exploring Human Consciousness
Lecture 5: Happiness and Prosperity – Current Scenario
Lecture 6: Method to Fulfill the Basic Human Aspirations
Tutorial 3: Practice Session PS3 Exploring Natural Acceptance

Expected outcome:
The students start exploring themselves: get comfortable with each other and with the teacher; they start appreciating the need and relevance for the course.

The students start finding that technical education without study of human values can generate more problems than solutions. They also start feeling that lack of understanding of human values is the root cause of most of the present-day problems; and a sustained solution could emerge only through understanding of value-based living. Any solution brought out through fear, temptation of dogma will not be sustainable.

The students are able to see that verification on the basis of natural acceptance and experiential validation through living is the only way to verify right or wrong, and referring to any external source like text or
instrument or any other person cannot enable them to verify with authenticity; it will only develop assumptions.

The students are able to see that their practice in living is not in harmony with their natural acceptance most of the time, and all they need to do is to refer to their natural acceptance to overcome this disharmony.

The students are able to see that lack of right understanding leading to lack of relationship is the major cause of problems in their family and not the lack of physical facility in most of the cases, while they have given higher priority to earning of physical facility in their life giving less value to or even ignoring relationships and not being aware that right understanding is the most important requirement for any human being.

Module 2 – Harmony in the Human Being (6 lectures and 3 tutorials for practice session)

**Lecture 7:** Understanding Human being as the Co-existence of the Self and the Body

**Lecture 8:** Distinguishing between the Needs of the Self and the Body

**Tutorial 4: Practice Session PS4** Exploring the difference of Needs of Self and Body

**Lecture 9:** The Body as an Instrument of the Self

**Lecture 10:** Understanding Harmony in the Self

**Tutorial 5: Practice Session PS5** Exploring Sources of Imagination in the Self

**Lecture 11:** Harmony of the Self with the Body

**Lecture 12:** Programme to ensure self-regulation and Health

**Tutorial 6: Practice Session PS6** Exploring Harmony of Self with the Body

**Expected outcome:**

The students are able to see that they can enlist their desires and the desires are not vague. Also they are able to relate their desires to ‘I’ and ‘Body’ distinctly. If any desire appears related to both, they are able to see that the feeling is related to I while the physical facility is related to the body. They are also able to see that ‘I’ and Body are two realities, and most of their desires are related to ‘I’ and not body, while their efforts are mostly centered on the fulfilment of the needs of the body assuming that it will meet the needs of ‘I’ too.

The students are able to see that all physical facility they are required for a limited time in a limited quantity. Also, they are able to see that in case of feelings, they want continuity of the naturally acceptable feelings and they do not want feelings which are not naturally acceptable even for a single moment.

The students are able to see that activities like understanding, desire, though and selection are the activities of ‘I’ only the activities like breathing, palpitation of different parts of the body are fully the activities of the body with the acceptance of ‘I’ while the activities they do with their sense organs like hearing through ears, seeing through eyes, sensing through touch, tasting through tongue and smelling through nose or the activities they do with their work organs like hands, legs etc. are such activities that require the participation of both ‘I’ and body.

The students become aware of their activities of ‘I’ and start finding their focus of attention at different moments. Also they are able to see that most of their desires are coming from outside (through preconditioning or sensation) and are not based on their natural acceptance
The students are able to list down activities related to proper upkeep of the body and practice them in their daily routine. They are also able to appreciate the plants wildly growing in and around the campus which can be beneficial in curing different diseases.

Module 3 – Harmony in the Family and Society (6 lectures and 3 tutorials for practice session)

- **Lecture 13:** Harmony in the Family – the Basic Unit of Human Interaction
- **Lecture 14:** 'Trust' – the Foundational Value in Relationship
- **Tutorial 7: Practice Session PS7** Exploring the Feeling of Trust
- **Lecture 15:** 'Respect' – as the Right Evaluation
- **Tutorial 8: Practice Session PS8** Exploring the Feeling of Respect
- **Lecture 16:** Other Feelings, Justice in Human-to-Human Relationship
- **Lecture 17:** Understanding Harmony in the Society
- **Lecture 18:** Vision for the Universal Human Order
- **Tutorial 9: Practice Session PS9** Exploring Systems to fulfil Human Goal

Expected outcome:

The students are able to note that the natural acceptance (intention) is always for living in harmony, only competence is lacking! We generally evaluate ourselves on the basis of our intention and others on the basis of their competence! We seldom look at our competence and others’ intention as a result we conclude that I am a good person and other is a bad person.

The students are able to see that respect is right evaluation, and only right evaluation leads to fulfilment in relationship. Many present problems in the society are an outcome of differentiation (lack of understanding of respect), like gender biasness, generation gap, caste conflicts, class struggle, dominations through power play, communal violence, clash of isms and so on so forth. All these problems can be solved by realizing that the other is like me as he has the same natural acceptance, potential and program to ensure a happy and prosperous life for them and for others through he may have different body, physical facility or beliefs.

The students are able to use their creativity for educating children. The students are able to see that they can play a role in providing value education for children. They are able to put in simple words the issues that are essential to understand for children and comprehensible to them. The students are able to develop an outline of holistic model for social science and compare it with the existing model.

Module 4 – Harmony in the Nature/Existence (4 lectures and 2 tutorials for practice session)

- **Lecture 19:** Understanding Harmony in the Nature
- **Lecture 20:** Interconnectedness, self-regulation and Mutual Fulfilment among the Four Orders of Nature
- **Tutorial 10: Practice Session PS10** Exploring the Four Orders of Nature
- **Lecture 21:** Realizing Existence as Co-existence at All Levels
- **Lecture 22:** The Holistic Perception of Harmony in Existence
- **Tutorial 11: Practice Session PS11** Exploring Co-existence in Existence
Expected outcome:
The students are able to differentiate between the characteristics and activities of different orders and study the mutual fulfillment among them. They are also able to see that human beings are not fulfilling to other orders today and need to take appropriate steps to ensure right participation (in terms of nurturing, protection and right utilization) in the nature. The students feel confident that they can understand the whole existence; nothing is a mystery in this existence. They are also able to see the interconnectedness in the nature, and point out how different courses of study relate to the different units and levels. Also, they are able to make out how these courses can be made appropriate and holistic.

Module 5 – Implications of the Holistic Understanding – a Look at Professional Ethics (6 lectures and 3 tutorials for practice session)

Lecture 23: Natural Acceptance of Human Values
Lecture 24: Definitiveness of (Ethical) Human Conduct
Tutorial 12: Practice Session PS12 Exploring Ethical Human Conduct
Lecture 25: A Basis for Humanistic Education, Humanistic Constitution and Universal Human Order
Lecture 26: Competence in Professional Ethics
Tutorial 13: Practice Session PS13 Exploring Humanistic Models in Education
Lecture 27: Holistic Technologies, Production Systems and Management Models-Typical Case Studies
Lecture 28: Strategies for Transition towards Value-based Life and Profession
Tutorial 14: Practice Session PS14 Exploring Steps of Transition towards Universal Human Order

Expected outcome:
The students are able to present sustainable solutions to the problems in society and nature. They are also able to see that these solutions are practicable and draw roadmaps to achieve them. The students are able to grasp the right utilization of their knowledge in their streams of Technology/Engineering/Management/any other area of study to ensure mutual fulfilment. E.g. mutually enriching production system with rest of nature. The students are able to sincerely evaluate the course and share with their friends. They are also able to suggest measures to make the course more effective and relevant. They are also able to make use of their understanding in the course for the happy and prosperous family and society.

Guidelines and Content for Practice Sessions (Tutorials)
In order to connect the content of the proposals with practice (living), 14 practice sessions have been designed. The full set of practice sessions is available in the Teacher’s Manual as well as the website.

Practice Sessions for Module 1 – Introduction to Value Education
PS1 Sharing about Oneself
AICTE Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology)

PS2 Exploring Human Consciousness
PS3 Exploring Natural Acceptance

Practice Sessions for Module 2 – Harmony in the Human Being
PS4 Exploring the difference of Needs of Self and Body
PS5 Exploring Sources of Imagination in the Self
PS6 Exploring Harmony of Self with the Body

Practice Sessions for Module 3 – Harmony in the Family and Society
PS7 Exploring the Feeling of Trust
PS8 Exploring the Feeling of Respect
PS9 Exploring Systems to fulfil Human Goal

Practice Sessions for Module 4 – Harmony in the Nature (Existence)
PS10 Exploring the Four Orders of Nature
PS11 Exploring Co-existence in Existence

Practice Sessions for Module 5 – Implications of the Holistic Understanding – a Look at Professional Ethics
PS12 Exploring Ethical Human Conduct
PS13 Exploring Humanistic Models in Education
PS14 Exploring Steps of Transition towards Universal Human Order

As an example, PS 7 is a practice session in module 3 regarding trust. It is explained below:

**PS 7:** Form small groups in the class and in that group initiate dialogue and ask the eight questions related to trust. The eight questions are:

1a. Do I want to make myself happy?  
1b. Am I able to make myself always happy?
2a. Do I want to make the other happy?  
2b. Am I able to make the other always happy?
3a. Does the other want to make him happy?  
3b. Is the other able to make him always happy?
4a. Does the other want to make me happy?  
4b. Is the other able to make me always happy?

Intention (Natural Acceptance)  
Competence  

What is the answer?  
What is the answer?

Let each student answer the questions for himself/herself and everyone else. Discuss the difference between intention and competence. Observe whether you evaluate your intention and competence as well as the others’ intention and competence.

**Expected outcome of PS 7:** The students are able to see that the first four questions are related to our Natural Acceptance i.e. intention and the next four to our Competence. They are able to note that the intention is always correct, only competence is lacking! We generally evaluate ourselves on the basis of
our intention and others on the basis of their competence! We seldom look at our competence and others’ intention, as a result we conclude that I am a good person and other is a bad person.

3-READINGS:

3-1-Text Book and Teachers Manual


3-2-Reference Books

3. The Story of Stuff (Book).
4. The Story of My Experiments with Truth - by Mohandas Karamchand Gandhi
5. Small is Beautiful - E. F Schumacher.
6. Slow is Beautiful - Cecile Andrews
7. Economy of Permanence - J C Kumarappa
8. Bharat Mein Angreji Raj – Pandit Sunderlal
9. Rediscovering India - by Dharampal
10. Hind Swaraj or Indian Home Rule - by Mohandas K. Gandhi
12. Vivekananda - Romain Rolland (English)
13. Gandhi - Romain Rolland (English)

4-MODE OF CONDUCT (L-T-P-C 2-1-0-3)

Lecture hours are to be used for interactive discussion, placing the proposals about the topics at hand and motivating students to reflect, explore and verify them.

Tutorial hours are to be used for practice sessions.

While analysing and discussing the topic, the faculty mentor’s role is in pointing to essential elements to help in sorting them out from the surface elements. In other words, help the students explore the important or critical elements.

In the discussions, particularly during practice sessions (tutorials), the mentor encourages the student to connect with one’s own self and do self-observation, self-reflection and self-exploration.
Scenarios may be used to initiate discussion. The student is encouraged to take up “ordinary” situations rather than “extra-ordinary” situations. Such observations and their analyses are shared and discussed with other students and faculty mentor, in a group sitting.

Tutorials (experiments or practical) are important for the course. The difference is that the laboratory is everyday life, and practical are how you behave and work in real life. Depending on the nature of topics, worksheets, home assignment and/or activity are included. The practice sessions (tutorials) would also provide support to a student in performing actions commensurate to his/her beliefs. It is intended that this would lead to development of commitment, namely behaving and working based on basic human values.

It is recommended that this content be placed before the student as it is, in the form of a basic foundation course, without including anything else or excluding any part of this content. Additional content may be offered in separate, higher courses.

**This course is to be taught by faculty from every teaching department.**

*Teacher preparation with a minimum exposure to at least one 8-day Faculty Development Program on Universal Human Values is deemed essential.*

5-SUGGESTED ASSESSMENT:

This is a compulsory credit course. The assessment is to provide a fair state of development of the student, so participation in classroom discussions, self-assessment, peer assessment etc. will be used in evaluation.

**Example:**

- Assessment by faculty mentor: 10 marks
- Self-assessment: 10 marks
- Assessment by peers: 10 marks
- Socially relevant project/Group Activities/Assignments: 20 marks
- Semester End Examination: 50 marks

The overall pass percentage is 40%. In case the student fails, he/she must repeat the course.

6-OUTCOME OF THE COURSE:

By the end of the course, students are expected to become more aware of themselves, and their surroundings (family, society, nature); they would become more responsible in life, and in handling problems with sustainable solutions, while keeping human relationships and human nature in mind. They would have better critical ability. They would also become sensitive to their commitment towards what they have understood (human values, human relationship and human society). It is hoped that they would be able to apply what they have learnt to their own self in different day-to-day settings in real life, at least a beginning would be made in this direction.

Therefore, the course and further follow up is expected to positively impact common graduate attributes like:
1. Holistic vision of life
2. Socially responsible behaviour
3. Environmentally responsible work
4. Ethical human conduct
5. Having Competence and Capabilities for Maintaining Health and Hygiene
6. Appreciation and aspiration for excellence (merit) and gratitude for all

This is only an introductory foundational input. It would be desirable to follow it up by
a) Faculty-student or mentor-mentee programs throughout their time with the institution
b) Higher level courses on human values in every aspect of living.

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<table>
<thead>
<tr>
<th>Course Code</th>
<th>: AU-02</th>
</tr>
</thead>
<tbody>
<tr>
<td>Course Title</td>
<td>: Sports and Yoga</td>
</tr>
<tr>
<td>Number of Credits</td>
<td>: 0 (L: 2^, T: 0, P: 0)</td>
</tr>
<tr>
<td>Course Category</td>
<td>: AU</td>
</tr>
</tbody>
</table>

Course Objective(s):

- To make the students understand the importance of sound health and fitness principles as they relate to better health.
- To expose the students to a variety of physical and yogic activities aimed at stimulating their continued inquiry about Yoga, physical education, health and fitness.
- To create a safe, progressive, methodical and efficient activity based plan to enhance improvement and minimize risk of injury.
- To develop among students an appreciation of physical activity as a lifetime pursuit and a means to better health.

Course Contents:

**Module I: Introduction to Physical Education**
- Meaning & definition of Physical Education
- Aims & Objectives of Physical Education
- Changing trends in Physical Education
Module II: Olympic Movement
- Ancient & Modern Olympics (Summer & Winter)
- Olympic Symbols, Ideals, Objectives & Values
- Awards and Honours in the field of Sports in India (Dronacharya Award, Arjuna Award, Dhyan Chand Award, Rajiv Gandhi Khel Ratna Award etc.)

Module III: Physical Fitness, Wellness & Lifestyle
- Meaning & Importance of Physical Fitness & Wellness
- Components of Physical fitness
- Components of Health related fitness
- Components of wellness
- Preventing Health Threats through Lifestyle Change
- Concept of Positive Lifestyle

Module IV: Fundamentals of Anatomy & Physiology in Physical Education, Sports and Yoga
- Define Anatomy, Physiology & Its Importance

Module V: Kinesiology, Biomechanics & Sports
- Meaning & Importance of Kinesiology & Biomechanics in Physical Edu. & Sports
- Newton’s Law of Motion & its application in sports.
- Friction and its effects in Sports.

Module VI: Postures
- Meaning and Concept of Postures.
- Causes of Bad Posture.
- Advantages & disadvantages of weight training.
- Concept & advantages of Correct Posture.
- Common Postural Deformities – Knock Knee; Flat Foot; Round Shoulders; Lordosis, Kyphosis, Bow Legs and Scoliosis.
- Corrective Measures for Postural Deformities

Module VII: Yoga
- Meaning & Importance of Yoga
- Elements of Yoga
- Introduction - Asanas, Pranayama, Meditation & Yogic Kriyas
- Yoga for concentration & related Asanas (Sukhasana; Tadasana; Padmasana & Shashankasana)
- Relaxation Techniques for improving concentration - Yog-nidra
Module VIII: Yoga & Lifestyle
- Asanas as preventive measures.
- Hypertension: Tadasana, Vajrasana, Pavan Muktasana, Ardha Chakrasana, Bhujangasana, Sharasana.
- Back Pain: Tadasana, Ardh Matsyendrasana, Vakrasana, Shalabhasana, Bhujangasana.

Module IX: Training and Planning in Sports
- Meaning of Training
- Warming up and limbering down
- Skill, Technique & Style
- Meaning and Objectives of Planning.
- Tournament – Knock-Out, League/Round Robin & Combination.

Module X: Psychology & Sports
- Definition & Importance of Psychology in Physical Edu. & Sports
- Define & Differentiate Between Growth & Development
- Adolescent Problems & Their Management
- Emotion: Concept, Type & Controlling of emotions
- Meaning, Concept & Types of Aggressions in Sports.
- Psychological benefits of exercise.
- Anxiety & Fear and its effects on Sports Performance.
- Motivation, its type & techniques.
- Understanding Stress & Coping Strategies.

Module XI: Doping
- Meaning and Concept of Doping
- Prohibited Substances & Methods
- Side Effects of Prohibited Substances

Module XII: Sports Medicine
- First Aid – Definition, Aims & Objectives.
- Sports injuries: Classification, Causes & Prevention.
- Management of Injuries: Soft Tissue Injuries and Bone & Joint Injuries
Module XIII: Sports / Games

Following subtopics related to any one Game/Sport of choice of student out of:
Athletics, Badminton, Basketball, Chess, Cricket, Kabaddi, Lawn Tennis,
Swimming, Table Tennis, Volleyball, Yoga etc.
- History of the Game/Sport.
- Specifications of Play Fields and Related Sports Equipment.
- Important Tournaments and Venues.
- Sports Personalities.
- Proper Sports Gear and its Importance.

Text Books/References:
1. Modern Trends and Physical Education by Prof. Ajmer Singh.
2. Light On Yoga By B.K.S. Iyengar.
3. Health and Physical Education – NCERT (11th and 12th Classes)

Course Outcomes: On successful completion of the course the students will be able:
1. To practice Physical activities and Hatha Yoga focusing on yoga for strength, flexibility, and relaxation.
2. To learn techniques for increasing concentration and decreasing anxiety which leads to stronger academic performance.
3. To learn breathing exercises and healthy fitness activities.
4. To understand basic skills associated with yoga and physical activities including strength and flexibility, balance and coordination.
5. To perform yoga movements in various combination and forms.
6. To assess current personal fitness levels.
7. To identify opportunities for participation in yoga and sports activities.
8. To develop understanding of health-related fitness components: cardiorespiratory endurance, flexibility and body composition etc.
9. To improve personal fitness through participation in sports and yogic activities.
10. To develop understanding of psychological problems associated with the age and lifestyle.
11. To demonstrate an understanding of sound nutritional practices as related to health and physical performance.
12. To assess yoga activities in terms of fitness value.
13. To identify and apply injury prevention principles related to yoga and physical fitness activities.
14. To understand and correctly apply biomechanical and physiological principles related to exercise and training.

*******
Pre-requisites: None. Universal Human Values 1 (Desirable)

1-COURSES ON HUMAN VALUES

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Objectives of UHV-II Course

This introductory course input is intended:

4. To help the students appreciate the essential complementarily between 'VALUES' and 'SKILLS' to ensure sustained happiness and prosperity which are the core aspirations of all human beings.
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The salient features of this course are:

5. It presents a universal approach to value education by developing the right understanding of reality (i.e. a worldview of the reality “as it is”) through the process of self-exploration.
6. The whole course is presented in the form of a dialogue whereby a set of proposals about various aspects of the reality are presented and the students are encouraged to self-explore the proposals by verifying them on the basis of their natural acceptance within oneself and validate experientially in living.
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8. The course is in the form of 28 lectures (discussions) and 14 practice sessions.
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10. It is a process of self-investigation and self-exploration, and not of giving sermons. Whatever is found as truth or reality is stated as a proposal and the students are facilitated to verify it in their own right, based on their Natural Acceptance and subsequent Experiential Validation – the whole existence is the lab and every activity is a source of reflection.

11. This process of self-exploration takes the form of a dialogue between the teacher and the students to begin with, and then to continue within the student in every activity, leading to continuous self-evolution.

12. This self-exploration also enables them to critically evaluate their pre-conditionings and present beliefs.

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**Lecture 2:** Understanding Value Education

**Tutorial 1: Practice Session PS1**  Sharing about Oneself

**Lecture 3:** Self-exploration as the Process for Value Education

**Lecture 4:** Continuous Happiness and Prosperity – the Basic Human Aspirations

**Tutorial 2: Practice Session PS2**  Exploring Human Consciousness

**Lecture 5:** Happiness and Prosperity – Current Scenario

**Lecture 6:** Method to Fulfill the Basic Human Aspirations

**Tutorial 3: Practice Session PS3**  Exploring Natural Acceptance

Expected outcome:

The students start exploring themselves: get comfortable with each other and with the teacher; they start appreciating the need and relevance for the course.

The students start finding that technical education without study of human values can generate more problems than solutions. They also start feeling that lack of understanding of human values is the root cause of most of the present-day problems; and a sustained solution could emerge only through understanding of value-based living. Any solution brought out through fear, temptation of dogma will not be sustainable.

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Module 2 – Harmony in the Human Being (6 lectures and 3 tutorials for practice session)

Lecture 7: Understanding Human being as the Co-existence of the Self and the Body
Lecture 8: Distinguishing between the Needs of the Self and the Body
Tutorial 4: Practice Session PS4   Exploring the difference of Needs of Self and Body
Lecture 9: The Body as an Instrument of the Self
Lecture 10: Understanding Harmony in the Self
Tutorial 5: Practice Session PS5   Exploring Sources of Imagination in the Self
Lecture 11: Harmony of the Self with the Body
Lecture 12: Programme to ensure self-regulation and Health
Tutorial 6: Practice Session PS6   Exploring Harmony of Self with the Body

Expected outcome:

The students are able to see that they can enlist their desires and the desires are not vague. Also they are able to relate their desires to ‘I’ and ‘Body’ distinctly. If any desire appears related to both, they are able to see that the feeling is related to I while the physical facility is related to the body. They are also able to see that ‘I’ and Body are two realities, and most of their desires are related to ‘I’ and not body, while their efforts are mostly centered on the fulfilment of the needs of the body assuming that it will meet the needs of ‘I’ too. The students are able to see that all physical facility they are required for a limited time in a limited quantity. Also, they are able to see that in case of feelings, they want continuity of the naturally acceptable feelings and they do not want feelings which are not naturally acceptable even for a single moment. The students are able to see that activities like understanding, desire, though and selection are the activities of ‘I’ only the activities like breathing, palpitation of different parts of the body are fully the activities of the body with the acceptance of ‘I’ while the activities they do with their sense organs like hearing through ears, seeing through eyes, sensing through touch, tasting through tongue and smelling through nose or the activities they do with their work organs like hands, legs etc. are such activities that require the participation of both ‘I’ and body. The students become aware of their activities of ‘I’ and start finding their focus of attention at different moments. Also they are able to see that most of their desires are coming from outside (through preconditioning or sensation) and are not based on their natural acceptance.
The students are able to list down activities related to proper upkeep of the body and practice them in their daily routine. They are also able to appreciate the plants wildly growing in and around the campus which can be beneficial in curing different diseases.

Module 3 – Harmony in the Family and Society (6 lectures and 3 tutorials for practice session)

| Lecture 13: Harmony in the Family – the Basic Unit of Human Interaction |
| Lecture 14: 'Trust' – the Foundational Value in Relationship |
| Tutorial 7: Practice Session PS7 Exploring the Feeling of Trust |
| Lecture 15: 'Respect' – as the Right Evaluation |
| Tutorial 8: Practice Session PS8 Exploring the Feeling of Respect |
| Lecture 16: Other Feelings, Justice in Human-to-Human Relationship |
| Lecture 17: Understanding Harmony in the Society |
| Lecture 18: Vision for the Universal Human Order |
| Tutorial 9: Practice Session PS9 Exploring Systems to fulfil Human Goal |

Expected outcome:

The students are able to note that the natural acceptance (intention) is always for living in harmony, only competence is lacking! We generally evaluate ourselves on the basis of our intention and others on the basis of their competence! We seldom look at our competence and others’ intention as a result we conclude that I am a good person and other is a bad person. The students are able to see that respect is right evaluation, and only right evaluation leads to fulfilment in relationship. Many present problems in the society are an outcome of differentiation (lack of understanding of respect), like gender biasness, generation gap, caste conflicts, class struggle, dominations through power play, communal violence, clash of isms and so on so forth. All these problems can be solved by realizing that the other is like me as he has the same natural acceptance, potential and program to ensure a happy and prosperous life for them and for others through he may have different body, physical facility or beliefs. The students are able to use their creativity for education children. The students are able to see that they can play a role in providing value education for children. They are able to put in simple words the issues that are essential to understand for children and comprehensible to them. The students are able to develop an outline of holistic model for social science and compare it with the existing model.

Module 4 – Harmony in the Nature/Existence (4 lectures and 2 tutorials for practice session)

| Lecture 19: Understanding Harmony in the Nature |
| Lecture 20: Interconnectedness, self-regulation and Mutual Fulfilment among the Four Orders of Nature |
| Tutorial 10: Practice Session PS10 Exploring the Four Orders of Nature |
| Lecture 21: Realizing Existence as Co-existence at All Levels |
| Lecture 22: The Holistic Perception of Harmony in Existence |
| Tutorial 11: Practice Session PS11 Exploring Co-existence in Existence |
Expected outcome:

The students are able to differentiate between the characteristics and activities of different orders and study the mutual fulfilment among them. They are also able to see that human beings are not fulfilling to other orders today and need to take appropriate steps to ensure right participation (in terms of nurturing, protection and right utilization) in the nature.

The students feel confident that they can understand the whole existence; nothing is a mystery in this existence. They are also able to see the interconnectedness in the nature, and point out how different courses of study relate to the different units and levels. Also, they are able to make out how these courses can be made appropriate and holistic.

Module 5 – Implications of the Holistic Understanding – a Look at Professional Ethics (6 lectures and 3 tutorials for practice session)

Lecture 23: Natural Acceptance of Human Values
Lecture 24: Definitiveness of (Ethical) Human Conduct
Tutorial 12: Practice Session PS12 Exploring Ethical Human Conduct
Lecture 25: A Basis for Humanistic Education, Humanistic Constitution and Universal Human Order
Lecture 26: Competence in Professional Ethics
Tutorial 13: Practice Session PS13 Exploring Humanistic Models in Education
Lecture 27: Holistic Technologies, Production Systems and Management Models-Typical Case Studies
Lecture 28: Strategies for Transition towards Value-based Life and Profession
Tutorial 14: Practice Session PS14 Exploring Steps of Transition towards Universal Human Order

Expected outcome:

The students are able to present sustainable solutions to the problems in society and nature. They are also able to see that these solutions are practicable and draw roadmaps to achieve them.

The students are able to grasp the right utilization of their knowledge in their streams of Technology/Engineering/Management/any other area of study to ensure mutual fulfilment. E.g. mutually enriching production system with rest of nature.

The students are able to sincerely evaluate the course and share with their friends. They are also able to suggest measures to make the course more effective and relevant. They are also able to make use of their understanding in the course for the happy and prosperous family and society.

Guidelines and Content for Practice Sessions (Tutorials)

In order to connect the content of the proposals with practice (living), 14 practice sessions have been designed. The full set of practice sessions is available in the Teacher’s Manual as well as the website.

Practice Sessions for Module 1 – Introduction to Value Education
PS1 Sharing about Oneself
PS2 Exploring Human Consciousness
PS3 Exploring Natural Acceptance

Practice Sessions for Module 2 – Harmony in the Human Being
PS4 Exploring the difference of Needs of Self and Body
PS5 Exploring Sources of Imagination in the Self
PS6 Exploring Harmony of Self with the Body

Practice Sessions for Module 3 – Harmony in the Family and Society
PS7 Exploring the Feeling of Trust
PS8 Exploring the Feeling of Respect
PS9 Exploring Systems to fulfill Human Goal

Practice Sessions for Module 4 – Harmony in the Nature (Existence)
PS10 Exploring the Four Orders of Nature
PS11 Exploring Co-existence in Existence

Practice Sessions for Module 5 – Implications of the Holistic Understanding – a Look at Professional Ethics
PS12 Exploring Ethical Human Conduct
PS13 Exploring Humanistic Models in Education
PS14 Exploring Steps of Transition towards Universal Human Order

As an example, PS 7 is a practice session in module 3 regarding trust. It is explained below:

**PS 7:** Form small groups in the class and in that group initiate dialogue and ask the eight questions related to trust. The eight questions are:

1a. Do I want to make myself happy?    1b. Am I able to make myself always happy?
2a. Do I want to make the other happy?    2b. Am I able to make the other always happy?
3a. Does the other want to make him happy?  3b. Is the other able to make him always happy?
4a. Does the other want to make me happy?  4b. Is the other able to make me always happy?

Intention (Natural Acceptance)    Competence
What is the answer?    What is the answer?

Let each student answer the questions for himself/herself and everyone else. Discuss the difference between intention and competence. Observe whether you evaluate your intention and competence as well as the others’ intention and competence.

**Expected outcome of PS 7:** The students are able to see that the first four questions are related to our Natural Acceptance i.e. intention and the next four to our Competence. They are able to note that the intention is always correct, only competence is lacking! We generally evaluate ourselves on the basis of
our intention and others on the basis of their competence! We seldom look at our competence and others’ intention, as a result we conclude that I am a good person and other is a bad person.

3-READINGS:

3-1-Text Book and Teachers Manual


3-2-Reference Books

3. The Story of Stuff (Book).
4. The Story of My Experiments with Truth - by Mohandas Karamchand Gandhi
5. Small is Beautiful - E. F Schumacher.
6. Slow is Beautiful - Cecile Andrews
7. Economy of Permanence - J C Kumarappa
8. Bharat Mein Angreji Raj – Pandit Sunderlal
9. Rediscovering India - by Dharampal
10. Hind Swaraj or Indian Home Rule - by Mohandas K. Gandhi
12. Vivekananda - Romain Rolland (English)
13. Gandhi - Romain Rolland (English)

4-MODE OF CONDUCT (L-T-P-C 2-1-0-3)

Lecture hours are to be used for interactive discussion, placing the proposals about the topics at hand and motivating students to reflect, explore and verify them.

Tutorial hours are to be used for practice sessions.

While analysing and discussing the topic, the faculty mentor’s role is in pointing to essential elements to help in sorting them out from the surface elements. In other words, help the students explore the important or critical elements.

In the discussions, particularly during practice sessions (tutorials), the mentor encourages the student to connect with one’s own self and do self-observation, self-reflection and self-exploration.
Scenarios may be used to initiate discussion. The student is encouraged to take up “ordinary” situations rather than “extra-ordinary” situations. Such observations and their analyses are shared and discussed with other students and faculty mentor, in a group sitting.

Tutorials (experiments or practical) are important for the course. The difference is that the laboratory is everyday life, and practical are how you behave and work in real life. Depending on the nature of topics, worksheets, home assignment and/or activity are included. The practice sessions (tutorials) would also provide support to a student in performing actions commensurate to his/her beliefs. It is intended that this would lead to development of commitment, namely behaving and working based on basic human values.

It is recommended that this content be placed before the student as it is, in the form of a basic foundation course, without including anything else or excluding any part of this content. Additional content may be offered in separate, higher courses.

This course is to be taught by faculty from every teaching department.

Teacher preparation with a minimum exposure to at least one 8-day Faculty Development Program on Universal Human Values is deemed essential.

5-SUGGESTED ASSESSMENT:

This is a compulsory credit course. The assessment is to provide a fair state of development of the student, so participation in classroom discussions, self-assessment, peer assessment etc. will be used in evaluation.

Example:

Assessment by faculty mentor: 10 marks
Self-assessment: 10 marks
Assessment by peers: 10 marks
Socially relevant project/Group Activities/Assignments: 20 marks
Semester End Examination: 50 marks

The overall pass percentage is 40%. In case the student fails, he/she must repeat the course.

6-OUTCOME OF THE COURSE:

By the end of the course, students are expected to become more aware of themselves, and their surroundings (family, society, nature); they would become more responsible in life, and in handling problems with sustainable solutions, while keeping human relationships and human nature in mind. They would have better critical ability. They would also become sensitive to their commitment towards what they have understood (human values, human relationship and human society). It is hoped that they would be able to apply what they have learnt to their own self in different day-to-day settings in real life, at least a beginning would be made in this direction.
Therefore, the course and further follow up is expected to positively impact common graduate attributes like:

7. Holistic vision of life
8. Socially responsible behaviour
9. Environmentally responsible work
10. Ethical human conduct
11. Having Competence and Capabilities for Maintaining Health and Hygiene
12. Appreciation and aspiration for excellence (merit) and gratitude for all

This is only an introductory foundational input. It would be desirable to follow it up by

a) Faculty-student or mentor-mentee programs throughout their time with the institution
b) Higher level courses on human values in every aspect of living.

*******
SEMESTER – III
SEMESTER III

<table>
<thead>
<tr>
<th>Course Code</th>
<th>Course Title</th>
<th>Credits</th>
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<tr>
<td>XC-01</td>
<td>Electronic Devices</td>
<td>3L:0T:0P 3 credits</td>
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</tbody>
</table>

**Course Contents:**

Introduction to Semiconductor Physics: Review of Quantum Mechanics, Electrons in periodic Lattices, E-k diagrams. Energy bands in intrinsic and extrinsic silicon; Carrier transport: diffusion current, drift current, mobility and resistivity; sheet resistance, design of resistors

Generation and recombination of carriers; Poisson and continuity equation P-N junction characteristics, I-V characteristics, and small signal switching models; Avalanche breakdown, Zener diode, Schottky diode

Bipolar Junction Transistor, I-V characteristics, Ebers-Moll Model, MOS capacitor, C-V characteristics, MOSFET, I-V characteristics, and small signal models of MOS transistor, LED, photodiode and solar cell;

Integrated circuit fabrication process: oxidation, diffusion, ion implantation, photolithography, etching, chemical vapor deposition, sputtering, twin-tub CMOS process.

**NPTEL Reference:** [https://nptel.ac.in/courses/117106091](https://nptel.ac.in/courses/117106091)

**Text/Reference Books:**


**Course Outcomes:**

At the end of this course students will demonstrate the ability to

1. Understand the principles of semiconductor Physics
2. Understand and utilize the mathematical models of semiconductor junctions and MOS transistors for circuits and systems.

**XC-02: Electronic Devices Lab (0L:0T:2P) (1 credit)**

Hands-on experiments related to the course contents of XC01
Course Contents:

Logic Simplification and Combinational Logic Design: Review of Boolean Algebra and De Morgan’s Theorem, SOP & POS forms, Canonical forms, Karnaugh maps up to 6 variables, Binary codes, Code Conversion.

MSI devices like Comparators, Multiplexers, Encoder, Decoder, Driver & Multiplexed Display, Half and Full Adders, Subtractor, Serial and Parallel Adders, BCD Adder, Barrel shifter and ALU.


Logic Families and Semiconductor Memories: TTL NAND gate, Specifications, Noise margin, Propagation delay, fan-in, fan-out, Tristate TTL, ECL, CMOS families and their interfacing, Memory elements, Concept of Programmable logic devices like FPGA. Logic implementation using Programmable Devices.

VLSI Design flow: Design entry: Schematic, FSM & HDL, different modeling styles in VHDL, Data types and objects, Dataflow, Behavioral and Structural Modeling, Synthesis and Simulation VHDL constructs and codes for combinational and sequential circuits.

NPTEL Course (if any): https://nptel.ac.in/courses/117106086

Text/Reference Books:

Course outcomes:
At the end of this course students will demonstrate the ability to
1. Design and analyze combinational logic circuits
2. Design & analyze modular combinational circuits with MUX/DEMUX, Decoder, Encoder
3. Design & analyze synchronous sequential logic circuits
4. Use HDL & appropriate EDA tools for digital logic design and simulation

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XC-04: Digital System Design Laboratory [0L: 0T: 2P] (1 credit)

Hands-on experiments related to the course contents XC03

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| XC-05 | Signals and Systems | 3L:0T:0P | 3 credits |

Course Contents:

Signals and systems as seen in everyday life, and in various branches of engineering and science.

Energy and power signals, continuous and discrete time signals, continuous and discrete amplitude signals. System properties: linearity: additivity and homogeneity, shift-invariance, causality, stability, realizability.


Periodic and semi-periodic inputs to an LSI system, the notion of a frequency response and its relation to the impulse response, Fourier series representation, the Fourier Transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality. The Discrete-Time Fourier Transform (DTFT) and the Discrete Fourier Transform (DFT). Parseval's Theorem. The idea of signal space and orthogonal bases.

The Laplace Transform, notion of eigen functions of LSI systems, a basis of eigen functions, region of convergence, poles and zeros of system, Laplace domain analysis, solution to differential equations and system behavior.

The z-Transform for discrete time signals and systems- eigen functions, region of convergence, z-domain analysis.


NPTEL course (if any): https://nptel.ac.in/courses/10810410

Text/Reference books:

Course outcomes:
At the end of this course students will demonstrate the ability to

1. Analyze different types of signals
2. Represent continuous and discrete systems in time and frequency domain using different transforms
3. Investigate whether the system is stable
4. Sampling and reconstruction of a signal

Course Contents:

Node and Mesh Analysis, matrix approach of network containing voltage and current sources, and reactances, source transformation and duality. Network theorems: Superposition, reciprocity, Thevenin's, Norton's, Maximum power Transfer, compensation and Tallegen's theorem as applied to AC circuits. Trigonometric and exponential Fourier series: Discrete spectra and symmetry of waveform, steady state response of a network to non-sinusoidal periodic inputs, power factor, effective values, Fourier transform and continuous spectra, three phase unbalanced circuit and power calculation.

Laplace transforms and properties: Partial fractions, singularity functions, waveform synthesis, analysis of RC, RL, and RLC networks with and without initial conditions with Laplace transforms evaluation of initial conditions.

Transient behavior, concept of complex frequency, Driving points and transfer functions poles and zeros of admittance function, their properties, sinusoidal response from pole-zero locations, convolution theorem and Two four port network and interconnections, Behaviors of series and parallel resonant circuits, Introduction to band pass, low pass, high pass and band reject filters.

NPTEL Course (if any): https://nptel.ac.in/courses/108106075

Text/Reference Books:
1. Van, Valkenburg.; –Network analysisl; Prentice hall of India, 2000

Course Outcomes:
At the end of this course students will demonstrate the ability to

1. Understand basics electrical circuits with nodal and mesh analysis.
2. Appreciate electrical network theorems.
3. Apply Laplace Transform for steady state and transient analysis.
4. Determine different network functions.
5. Appreciate the frequency domain techniques.
XC-07 | Probability and Stochastic Processes | 3L:0T:0P | 3 credits

**Course Contents:**

Sets and set operations; Probability space; Conditional probability and Bayes theorem; Combinatorial probability and sampling models.

Discrete random variables, probability mass function, probability distribution function, example random variables and distributions; Continuous random variables, probability density function, probability distribution function, example distributions

Joint distributions, functions of one and two random variables, moments of random variables; Conditional distribution, densities and moments; Characteristic functions of a random variable; Markov, Chebyshev and Chernoff bounds;

Random sequences and modes of convergence (everywhere, almost everywhere, probability, distribution and mean square); Limit theorems; Strong and weak laws of large numbers, central limit theorem.


**NPTEL course (if any):** [https://archive.nptel.ac.in/courses/117/105/117105085/](https://archive.nptel.ac.in/courses/117/105/117105085/)

**Text/Reference Books:**


**Course Outcomes:**

At the end of this course students will demonstrate the ability to

1. Understand representation of random signals
2. Investigate characteristics of random processes
3. Make use of theorems related to random signals
4. To understand propagation of random signals in LTI systems.

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SEMESTER – IV
SEMESTER IV

| XC-08 | Analog circuits | 3L:0T:0P | 3 credits |

Course Contents:

Diode Circuits, Amplifier models: Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier. Biasing schemes for BJT and FET amplifiers, bias stability, various frequency transistor models, estimation of voltage gain, input resistance, output resistance etc., design procedure for particular specifications, low frequency analysis of multistage amplifiers.

High frequency transistor models, frequency response of single stage and multistage amplifiers, cascode amplifier. Various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues. Feedback topologies: Voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc., calculation with practical circuits, concept of stability, gain margin and phase margin.

Oscillators: Review of the basic concept, Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.), LC oscillators (Hartley, Colpitt, Clapp etc.), non-sinusoidal oscillators.

Current mirror: Basic topology and its variants, V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load. Differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR. OP-AMP design: design of differential amplifier for a given specification, design of gain stages and output stages, compensation.


Digital-to-analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc. Analog-to-digital converters (ADC): Single slope, dual slope, successive approximation, flash etc. Switched capacitor circuits: Basic concept, practical configurations, application in amplifier, integrator, ADC etc.

NPTEL course: https://nptel.ac.in/courses/108106084

Text/Reference Books:

Course Outcomes:
At the end of this course students will demonstrate the ability to
1. Understand the characteristics of diodes and transistors
2. Design and analyze various rectifier and amplifier circuits
3. Design sinusoidal and non-sinusoidal oscillators
4. Understand the functioning of OP-AMP and design OP-AMP based circuits
5. Design ADC and DAC

<table>
<thead>
<tr>
<th>XC-09</th>
<th>Analog circuits lab</th>
<th>0L:0T:2P</th>
<th>1 credit</th>
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Hands-on experiments related to the course contents XC08

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**Course Contents:**


Introduction to Microprocessor, Microprocessor architecture and its operations, Memory, Input & output devices, Logic devices for interfacing, The 8085 MPU, Example of an 8085 based computer, Memory interfacing. Basic interfacing concepts, interfacing output displays, Interfacing input devices, Memory mapped I/O,

Data Transfer operations, Arithmetic operations, Logic Operations, Branch operation, Addressing modes, Writing assembly language programs, Programming techniques: looping, counting and indexing. Additional data transfer and 16 bit arithmetic instruction, Arithmetic operations related to memory, Logic operation: rotate, compare, counter and time delays. Subroutines. Interrupts

8255 Programmable peripheral interface, interfacing keyboard and seven segment display, 8254 (8253) programmable interval timer, 8259A programmable interrupt controller, Direct Memory Access and 8237 DMA controller.

Microcontrollers and Embedded Processors. Architecture – Block diagram of 8051, Pin configuration, Registers, Internal Memory, Timers, Port Structures, Interrupts. Assembly Language Programming - Addressing Modes, Instruction set (Detailed study of 8051 instruction set is required).


**NPTEL course:** [https://nptel.ac.in/courses/106102157](https://nptel.ac.in/courses/106102157)

**Text/Reference Books:**

2. 8085 Microprocessor Architecture, Applications and Programming, Ramesh S Gaonkar, Penram International
3. The 8051 microcontroller and Embedded systems, Muhammed Ali Mazidi & Janice Gill Mazidi, Pearson Education

**Course Outcomes:**

At the end of the course, the students will be able to

1. Explain the functional units with respect to computer architecture
2. Develop simple programmes using 8085/8051 assembly language
3. Interface 8085 with peripherals using assembly language
4. Interface 8051 with peripherals using assembly language/C

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<tr>
<th>Lab</th>
<th>Duration</th>
<th>Credit</th>
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<tr>
<td>XC-11</td>
<td>Microcontrollers Lab</td>
<td>0L:0T:2P</td>
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Hands-on experiments related to the course contents XC10

*****
Course Contents:


Review of probability and random process. Gaussian and white noise characteristics, Noise in amplitude modulation systems, Noise in Frequency modulation systems. Pre-emphasis and De-emphasis, Threshold effect in angle modulation.


Text/Reference Books:

Course Outcomes:
At the end of this course students will demonstrate the ability to
1. Analyze and compare different analog modulation schemes for their efficiency and bandwidth
2. Analyze the behavior of a communication system in presence of noise
3. Investigate pulsed modulation system and analyze their system performance
4. Analyze different digital modulation schemes and can compute the bit error performance
Introduction to Microfabrication | 3L:0T:0P | 3 credits

Course Contents:

**Introduction**: History of IC’s; Operation & Models for Devices of Interest: CMOS and MEMS. Electronic Materials: Crystal Structures, Defects in Crystals, Si, Poly Si, Si Crystal Growth. Clean room and Wafer Cleaning: Definition, Need of Clean Room, RCA cleaning of Si.

**Oxidation**: Dry and Wet Oxidation, Kinetics of Oxidation, Oxidation Rate Constants, Dopant Redistribution, Oxide Charges, Device Isolation, LOCOS, Oxidation System

**Lithography**: Overview of Lithography, Radiation Sources, Masks, Photoresist, Components of Photoresist Optical Aligners, Resolution, Depth of Focus, Advanced Lithography: E-beam Lithography, X-ray Lithography, Ion Beam Lithography.


**Thin Film Deposition**: Physical Vapor Deposition: Thermal evaporation, Resistive Evaporation, Electron beam evaporation, Laser ablation, Sputtering Chemical Vapor Deposition: Advantages and disadvantages of Chemical Vapor deposition (CVD) techniques over PVD techniques, reaction types, Boundaries and Flow, Different kinds of CVD techniques: APCVD, LPCVD, Metalorganic CVD (MOCVD), Plasma Enhanced CVD etc.

**Etching**: Anisotropy, Selectivity, Wet Etching, Plasma Etching, Reactive Ion Etching.

Overview of Interconnects, Contacts, Metal gate/Poly Gate, Metallization, Problems in Aluminum Metal contacts, Al spike, Electromigration, Metal Silicides, Multi-Level Metallization, Planarization, Inter Metal Dielectric

**Text/Reference Books**:

**Course Objectives**:

At the end of this course students will demonstrate the ability to:

1. Elucidate the CMOS process flow
2. Analyze various critical processing steps in microfabrication
3. Appreciate the advanced methods involved in IC fabrication.
4. Analyze the advancements in CMOS process fabrication with scaling in technology.

*****

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Hands-on experiments related to the course contents XC-13

Course Contents:
System & Architectural Design: Defining a system specification, performance analysis, cost analysis, identifying various functional blocks/modules; categorizing them in terms of digital, analog, RF and mixed signal blocks

Functional verification, logic design: Verifying the functionality of blocks, behavioral description, logic minimization, synthesis, verification and testing

Circuit Optimization and Physical Design: Optimization of synthesized blocks for various performance metric, Introduction to placement and route, Layout Vs Schematic (LVS) verification, Design for Manufacturability.


Fabrication and Packaging: CMOS process flow, dicing, various types of packaging.

Text /Reference Books:

Course Outcomes:
At the end of this course students will demonstrate the ability to
1. Understand the intricacies in VLSI Design flow
2. Understand overall process of VLSI Design flow starting from system level all the way to the transistor level
AICTE Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology)
SEMESTER – V
AICTE Model Curriculum for UG Degree Course in Electronics Engineering (VLSI Design and Technology)
SEMESTER - V

<table>
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<tr>
<th>XC-16</th>
<th>Digital Signal Processing</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
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</table>

Course Contents:

Discrete time signals: Sequences; representation of signals on orthogonal basis; Sampling and reconstruction of signals; Discrete systems attributes, Z-Transform, Analysis of LSI systems, frequency Analysis, Inverse Systems, Discrete Fourier Transform (DFT), Fast Fourier Transform Algorithm, Implementation of Discrete Time Systems


Effect of finite register length in FIR filter design. Parametric and non-parametric spectral estimation. Introduction to multirate signal processing. Application of DSP.

Text/Reference Books:


Course Outcomes:

At the end of this course students will demonstrate the ability to

1. Represent signals mathematically in continuous and discrete time and frequency domain
2. Get the response of an LSI system to different signals
3. Design of different types of digital filters for various applications

*****
XC-17 Electromagnetic Waves 3L:0T:0P 3 credits

Course Contents:

Transmission Lines- Equations of Voltage and Current on TX line, Propagation constant and characteristic impedance, and reflection coefficient and VSWR, Impedance Transformation on Loss- less and Low loss Transmission line, Power transfer on TX line, Smith Chart, Admittance Smith Chart, Applications of transmission lines: Impedance Matching, use transmission line sections as circuit elements.


Uniform Plane Wave- Uniform plane wave, Propagation of wave, Wavepolarization, Poincare's Sphere, Wave propagation in conducting medium, phase and group velocity, Power flow and Poynting vector, Surface current and power loss in a conductor

Plane Waves at a Media Interface- Plane wave in arbitrary direction, Reflection and refraction at dielectric interface, Total internal reflection, wave polarization at media interface, Reflection from a conducting boundary.

Wave propagation in parallel planewaveguide, Analysis of waveguide general approach, Rectangular waveguide, Modal propagation in rectangular waveguide, Surface currents on the waveguide walls, Field visualization, Attenuation in waveguide.

Radiation: Solution for potential function, Radiation from the Hertz dipole, Powerradiated by hertz dipole, Radiation Parameters of antenna, receiving antenna, Monopole and Dipole antenna.

Text/Reference Books:
5. David Cheng, Electromagnetics, Prentice Hall

Course Outcomes:
At the end of this course students will demonstrate the ability to

1. Understand characteristics and wave propagation on high frequency transmission lines
2. Carryout impedance transformation on TL
3. Use sections of transmission line sections for realizing circuit elements
4. Characterize uniform plane wave
5. Calculate reflection and transmission of waves at media interface
6. Analyze wave propagation on metallic waveguides in modal form
7. Understand principle of radiation and radiation characteristics of an antenna

*****
Hands-on experiments related to the course contents XC16

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Course Contents:

Overview of VLSI Design: Historical perspective, overview of VLSI design methodologies, VLSI design flow, design hierarchy, concepts of regularity, modularity, and locality, VLSI design styles, design quality, packaging technology, CAD technology.

MOS Transistor Theory: Introduction to The metal oxide semiconductor (MOS) structure, Long-channel I-V characteristics, C-V characteristics, non-linear I-V effects, DC transfer characteristics.

Introduction to ASIC and SoC, Overview of ASIC flow, functional verification, RTL-GATE level synthesis, synthesis optimization techniques, pre-layout timing verification, static timing analysis, floor-planning, placement and routing, extraction, post layout timing verification, extraction.

CMOS Process Technology: Fabrication process flow- basic steps, the CMOS n-Well process, layout design rules, stick diagram, full-custom mask layout design

MOS Inverter (Static Characteristics): Resistive-load inverter, inverter with n-type 16 MOSFET load, CMOS inverter

MOS Inverters (Switching Characteristics and Interconnects effects): Delay-time definitions, calculation of delay times, logical efforts, inverter design with delay constraints, estimation of interconnect parasitics, calculation of interconnect delay, Bus vs. Network-onChip (NoC), switching power dissipation of CMOS inverters.

Combination CMOS Logic Circuits: MOS logic circuits with depletion nMOS loads, CMOS logic circuits, complex logic circuits, CMOS transmission gates (pass gates), ratioed, dynamic and pass transistor logic circuits.

Sequential MOS logic circuits: Behaviour of bi-stable elements, SR latch circuits, clocked latch and flip-flop circuits, CMOS D-latch and edge-triggered flip-flop. Timing path, Setup time and hold time static, example of setup and hold time static, setup and hold slack, clock skew and jitter, Clock, reset and power distributions.

Semiconductor Memories: Memory Design, SRAM, DRAM structure and implementations


Text/Reference Books:


Course Outcomes:
At the end of this course students will demonstrate the ability to
1. provide an overview of the digital IC design techniques
2. Understand the characteristics of CMOS inverter.
3. Analyze the static and dynamic characteristics of CMOS circuits
4. Design and implementation of combinational and sequential circuits
5. Evaluate the performance of CMOS circuits

XC-20  VLSI Design Lab  0L:0T:2P  1 credit

Hands-on experiments related to the course contents XC16

XC-21  Control Systems  3L:0T:0P  3 credits

Course Contents:


State variable Analysis- Concepts of state, state variable, state model, state models for linear continuous time functions, diagonalization of transfer function, solution of state equations, concept of controllability & observability.

Text/Reference Books:
5. Nagrath & Gopal, --Modern Control Engineering, New Age International, New Delhi

Course Outcomes:
At the end of this course students will demonstrate the ability to:
1. Characterize a system and find its study state behavior
2. Investigate stability of a system using different tests
3. Design various controllers
4. Solve linear, non-linear and optimal control problems

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<table>
<thead>
<tr>
<th>XC-22</th>
<th>Embedded Systems</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

Course Contents:
Introduction to Embedded systems: Motivation based on applications of embedded systems, Basics of Embedded systems, functional block

Modeling of Embedded system: Mathematical modeling of physical systems to fit into embedded systems, Continuous Dynamics, Discrete Dynamics, Hybrid Systems, actor models, Composition of State Machines

Microcontrollers, Sensors, Actuators, Basics of Microcontrollers, 8951, Arduino microcontroller development board, I/Os, Sensors, Actuators

Interfacing between analog and digital blocks, signal conditioning, digital signal processing, sub-system interfacing, interfacing with external systems, user interfacing. Design tradeoffs due to process compatibility, thermal considerations, etc., Software aspects of embedded systems: real time programming languages and operating systems for embedded systems.

Text/Reference Books:

Course Outcomes:
At the end of the course, students will demonstrate the ability to:
1. Suggest design approach using advanced controllers to real-life situations.
2. Design interfacing of the systems with other data handling / processing systems.
3. Appreciate engineering constraints like energy dissipation, data exchange speeds etc.

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<table>
<thead>
<tr>
<th>XC-23</th>
<th>Embedded Systems lab</th>
<th>0L:0T:2P</th>
<th>1 credits</th>
</tr>
</thead>
</table>

Hands-on experiments related to the course contents XC21

*****
SEMESTER – VI
SEMESTER VI

<table>
<thead>
<tr>
<th>XC-24</th>
<th>VLSI Verification and Testing</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

Course Contents:

Physical faults and their modeling. Fault equivalence and dominance; fault collapsing, Fault simulation: parallel, deductive and concurrent techniques; critical path tracing.

Test generation for combinational circuits: Boolean difference, D-algorithm, Podem, random etc. Exhaustive, random and weighted test pattern generation; aliasing and its effect on fault coverage.

PLA testing: cross-point fault model, test generation, easily testable designs.

Memory testing: permanent, intermittent and pattern-sensitive faults; test generation.

Delay faults and hazards; test pattern generation techniques, ATPG and its different types

Test pattern generation for sequential circuits: ad-hoc and structures techniques scan path and LSSD, boundary scan

Built-in self-test techniques: LBIST and MBIST. Verification: logic level (combinational and sequential circuits), RTL-level (data path and control path). Verification of embedded systems. Use of formal techniques: decision diagrams, logic-based approaches.

ASIC/IP Verification, direct and random testing, Error detection and correction codes.

Text/Reference Books:


Course Outcomes:

1. Extend knowledge of the requirement of fault modeling in VLSI circuits.
2. Generate test vectors to test a circuit efficiently covering maximum faults.
3. Demonstrate the concept of Memory testing techniques
4. Discuss about Built-in-Self Test and its application in modern digital design
5. Use modern tools for testing and verification.
Hands-on experiments related to the course contents XC23

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**Course Contents:**

**Fundamentals of vacuum technology** - nomenclature and definition, pressure regions, gas properties and laws, molecular processes and kinetic theory, gas flow calculations, technology of vacuum pumps - throughput, pumping speed, forevacuum and high vacuum pumping, pump system design, diaphragm pumps, vacuum blowers, diffusion pumps, cryogenic pumps, turbomolecular pumps, pumps for ultra-high vacuum, vacuum measurements, types of gauges, mass analysis and spectrometry, mass flow control and measurement, vacuum valves, flanges and components, vacuum feedthroughs, vacuum seals, vacuum leak detectors, vacuum chambers and viewports, outgassing, vacuum applications such as sputtering, plasma etching, CVD, epitaxy, electron spectroscopies

**Plasma Science and Technology**

Plasma physics - Motion of individual electrons and ions in electric and magnetic fields - Single, collisionless, particles in DC and AC electric fields, Particle orbits in magnetic fields, Space charge and collective effects, Debye shielding, Plasma oscillations and plasma frequency, Plasma shielding and plasma sheaths, Response to DC, RF and microwave fields, Plasma potential, Characteristic electron and ion transit times

Introduction to Plasma Reactors - Chamber pump systems, load locks, mass flow control, hazardous gas handling, effluent control, Pressure gauges / control (Piranhi, thermocouple, ionization, baratron, convectron) Wafer chucks (Clamps/Electrostatic chucks) RF and microwave power sources and coupling - Power sources, matching networks, feedthroughs and coupling

RF Capacitively and Inductively coupled plasmas - Spatial variations of plasma potential, electric field, charge density and energy, optical emission, Sheaths at powered, grounded and floating surfaces, parameters, models, matching networks, Ion bombardment - energy / time / frequency / power dependencies

Applications in processes - etching, deposition, sputtering, ashing
Text/Reference Books:

3. Handbook of Vacuum Technology: Karl Jousten, Wiley
5. Plasma Etching in Semiconductor Fabrication- Russ Morgan, Elsevier
6. Fundamentals of Plasma Physics- J. A. Bittencourt, Springer India

Course Outcome:

1. Understand Basics of Vacuum Technology
2. Understand Basics of Plasma Technology
3. Ability to analyze vacuum and plasma based semiconductor equipment

*****
XC-27 | Semiconductor materials synthesis and characterization | 3L:0T:0P | 3 credits

Course Contents:


Production of metallurgical grade (MG) Si: Carbothermic reduction, principle, operation and practice of sub-merged arc furnace, energy and process calculation, refining & impurities control in molten MG Si. Production of electronic grade (EG) Si: Concept of fluidized bed reactor, Siemens Process. Crystal Growth: Crystal growth processes (Bridgman and its variants, Czochralski), heat and species transfer during non-steady and steady state plane-front growth, interface instability and effect of convection on interface stability

XRD (Bulk and thin film), Microscopy (Optical, SEM, TEM, SPM), UV-Visible spectroscopy, Photoluminescence, Raman spectroscopy

Text/Reference Books:


Course Outcome:

1. Understand the Silicon extraction and purification process
2. Understand Crystallography of Si and various methods of growth
3. Understand key methods of physicochemical, morphological and analytical characterization techniques

*****
Appendix
### Appendix - 1

**Program Elective Courses:**

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Course Code</th>
<th>Course Title</th>
<th>Preferred Semester</th>
<th>Hrs /Week L: T: P</th>
<th>Credits</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>XCEL1</td>
<td>Analog IC Design</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>XCEL2</td>
<td>Semiconductor Device Modeling</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>3</td>
<td>XCEL3</td>
<td>CAD for VLSI</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>4</td>
<td>XCEL4</td>
<td>Semiconductor Optoelectronics</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>XCEL5</td>
<td>Introduction to MEMS</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td>XCEL6</td>
<td>Semiconductor Packaging and Testing</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td>XCEL7</td>
<td>Mixed Signal Design</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>8</td>
<td>XCEL8</td>
<td>Bio-Medical Electronics</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>9</td>
<td>XCEL9</td>
<td>RF Microelectronics</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>10</td>
<td>XCEL10</td>
<td>Digital Image Processing</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>11</td>
<td>XCEL11</td>
<td>Low Power VLSI</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>12</td>
<td>XCEL12</td>
<td>High Speed Interfacing Circuits</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>13</td>
<td>XCEL13</td>
<td>Power Converters Design</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>14</td>
<td>XCEL14</td>
<td>Organic Electronics</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>15</td>
<td>XCEL15</td>
<td>Flexible Electronics</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>16</td>
<td>XCEL16</td>
<td>Nanoelectronics</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>17</td>
<td>XCEL17</td>
<td>Heterojunction Device Physics</td>
<td>VIII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>18</td>
<td>XCEL18</td>
<td>Internet of Things</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>No</td>
<td>Code</td>
<td>Course Title</td>
<td>Semester</td>
<td>Credits</td>
<td>Total</td>
</tr>
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<td>-------</td>
</tr>
<tr>
<td>19</td>
<td>XCEL19</td>
<td>Cyber Security</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>20</td>
<td>XCEL20</td>
<td>Artificial Intelligence and Machine Learning</td>
<td>VII</td>
<td>3:0:0</td>
<td>3</td>
</tr>
<tr>
<td>21</td>
<td>XCEL21</td>
<td>Quantum Computing</td>
<td>VI</td>
<td>3:0:0</td>
<td>3</td>
</tr>
</tbody>
</table>
Detailed Contents:

Introduction to MOSFETS, Simple MOSFET circuits, Threshold voltage model, Capacitance model, Mobility model, MOSFET basics, Basic current mirrors, Cascode current mirrors, Active current mirrors with large and small signal analysis, MOSFET in integrated circuits, Common mode properties.


Feedback topologies (voltage-voltage, current-voltage, voltage-current, current-voltage) and the noise and the loading effect analysis, Negative feedback, Stability of negative feedback systems, Stability and frequency compensation: Specification analysis, multi-pole system, three-stage opamp, phase margin Frequency compensation, pole-zero doublet analysis.

Design of the CMOS operational amplifiers: One-stage opamps and two-stage opamps, Gain boosting techniques, folded cascode, telescopic amplifier, common mode feedback (CMFB) amplifier, Three-stage opamp architectures, opamp specifications analysis, Design of high-speed and high-gain amplifiers.

CMOS amplifier Frequency response: Miller effect, common source (CS), common gate (CG), common drain (CD) stages, and cascode stage Analog layout techniques for MIM, MOM and fringe capacitor.

NPTEL course (if any): https://nptel.ac.in/courses/108106105.

Text/Reference Books:

Course Outcomes: At the end of this course, students will demonstrate the ability to
1. Realize the concepts of Analog MOS devices and current mirror circuits.
2. Design different configurations of Amplifiers and feedback circuits.
3. Analyze the characteristics of the frequency response of the amplifier and its noise.
Detailed Contents:


Field-Effect Transistors: JFET- current-voltage characteristics, effects in real devices, high-frequency and high-speed issues, Metal Insulator Semiconductor FET.

MOSFET- basic operation and fabrication, ideal MOS capacitor, Energy band diagram in equilibrium and under bias, Flat band voltage, Potential Balance and charge balance, Effect of gate body voltage on surface condition, Accumulation and depletion, Inversion, CV Characteristics, Frequency response, threshold voltages, output and transfer characteristics of MOSFET, short channel and Narrow width effects, MOSFET scaling.

Optoelectronics Devices: Light emitting diodes, Lasers, Photoconductors, Junction Photodiodes, Avalanche Photodiodes, Solar Cells, SPICE Models for Semiconductor Devices: MOSFET Level 1, Level 2 and level 3 model, Model parameters; SPICE models of p-n diode and BJT.

Text/Reference Books:

Course Outcomes:
At the end of this course, students will demonstrate the ability to
1. Explain the equations, approximations, and techniques available for deriving a model with specified properties, for a general device characteristic with known qualitative theory.
2. Apply suitable approximations and techniques to derive the model starting from drift-diffusion transport equations (assuming these equations hold).
3. Offer clues to a qualitative understanding of the physics of a new device and conversion of this understanding into equations.
4. Simulate characteristics of a simple device using MATLAB, and SPICE tools.

Detailed Contents:
Overview of digital logic design, Simplification of switching functions, K-map-based reduction of switching functions.

Combinational logic design, Complex combinational logic modules such as multiplexers/demultiplexers, decoders, PLAs, and their use in standardized combinational logic design.

Memory elements and time delay concepts, Flip-flops, latches, registers; Sequential circuit concepts and state diagrams; Clock-mode sequential circuits analysis and design; Synthesis of state diagrams; Fundamental-mode sequential circuits.

Analysis and design, hazards, races, and cycles. Logic element realization: Ideal switch based implementation; Logic families; FET switches; MOS switch-based logic realization; NMOS and CMOS logic-Pass transistor logic; Algorithmic optimization of combinational logic; VLSI realization of combinational logic.

Language-based description of complex digital systems; RTL descriptions and design language representation; Levels of description; Behavioral and structural descriptions; VHDL and Verilog.

Text/Reference Books:
Course Outcomes:
At the end of this course students will demonstrate the ability to
1. This course deals with the fundamentals of Computer-Aided Design (CAD) tools for the design, analysis, synthesis, test, verification.
2. Design and analysis of Computer-Aided Design (CAD) tools for the routing and placement of digital Very Large-Scale Integration (VLSI) systems.

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XCEL4 Semiconductor Optoelectronics 3L:0T:0P 3 credits

Detailed Contents:

Interaction of photons with electrons and holes in a semiconductor: Rates of emission and absorption, Condition for amplification by stimulated emission, the laser amplifier.

Semiconductor Photon Sources: Electroluminescence. The LED: Device structure, materials and characteristics. The Semiconductor Laser: Basic structure, theory, and device characteristics, direct current modulation, Quantum-well lasers; DFB-, DBR- and vertical-cavity surface-emitting lasers (VCSEL), Laser diode arrays, Device packages, and handling.

Semiconductor Optical Amplifiers & Modulators: Semiconductor optical amplifiers (SOA), SOA, characteristics and some applications, Quantum-confined Stark Effect and Electro-Absorption Modulators.


NPTEL course (if any): https://nptel.ac.in/courses/115102026.

Text/Reference Books:
Course Outcomes:

At the end of this course, students will demonstrate the ability to

1. Acquire a fundamental understanding of the basic physics behind optoelectronic devices.
2. Develop a basic understanding of light-emitting diodes.
3. Develop detailed knowledge of laser operating principles and structures.
4. Acquire a depth understanding of photodetectors.
5. Acquire detailed knowledge of solar cells and optoelectronic modulation and switching devices.
6. Develop a basic understanding of optoelectronic integrated circuits.

*****

| XCEL5 | Introduction to MEMS | 3L:0T:0P | 3 credits |

Detailed Contents:

Introduction to MEMS: Historical Background, Scaling Effects. Micro/Nano Sensors, Review of Basic MEMS fabrication modules, Oxidation, Deposition Techniques, Lithography (LIGA), and Etching.

Micromachining, Surface Micromachining, sacrificial layer processes, Stiction, Bulk Micromachining, Isotropic Etching, and Anisotropic Etching, Wafer Bonding, Mechanics of solids in MEMS/NEMS.

Micro-actuators and Micro-sensors: Micro-sensors, acoustic wave sensors, biomedical and nano-sensors, chemical sensors, optical sensors, pressure sensors, thermal sensors, micro-actuation through thermal forces, SMA-Piezo electric crystals, and electrostatic forces, magnetic actuation, micro-grippers, micro-motors, micro-valves, micropumps, micro-accelerometers.


Case studies of MEMS Products: Micro-fluidic devices, micro/nano transducers, blood pressure sensors, microphone-acceleration sensors, gyroscope, an overview of micro-system packaging.

NPTEL course (if any): [https://nptel.ac.in/courses/117105082](https://nptel.ac.in/courses/117105082).

Text/Reference Books:

1. Marc Madou, Fundamentals of Microfabrication and Nanotechnology (3 rd edition);
2. Stephen D. Senturia, Microsystem Design.
Course Outcomes:

At the end of this course, students will demonstrate the ability to

1. Understand fundamental principles of sensing and actuation and corresponding scaling laws in MEMS.
2. Gain a comprehensive perspective of various fabrication processes and materials used in microfabrication.
3. Understand the principle, design, and fabrication techniques of leading exemplary devices in the MEMS industry.
4. Design basic MEMS devices using relevant mechanical/electrical/fluidic engineering principles.

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XCEL6  |  Semiconductor Packaging and Testing  |  3L:0T:0P  |  3 credits

Detailed Contents:

Overview of electronic systems packaging: Introduction and Objectives of the course definition of a system and history of semiconductors, Products and levels of packaging, Packaging aspects of handheld products, Case studies in the application.

Semiconductor Packaging Overview: Basics of Semiconductor and Process flowchart; Video on “Sand-to-Silicon”, Wafer fabrication, inspection and testing, Wafer packaging; Packaging evolution, Chip connection choices, Wire bonding, TAB and flipchip-1, TAB and flipchip-2, Need for packaging & Single chip packages or modules (SCM), Commonly used packages and advanced packages, Materials in packages, Thermal mismatch in packages, Current trends in packaging, Multichip modules (MCM)-type, System-in-package (SIP), Packaging roadmaps, Hybrid circuits.

Electrical Design considerations in systems packaging (L. Umanand): Electrical Issues – I Resistive Parasitic, Electrical Issues – II; Capacitive and Inductive Parasitic, Electrical Issues – III; Layout guidelines and the Reflection problem, Electrical Issues – IV; Interconnection, CAD for Printed Wiring Boards: Benefits from CAD; Introduction to DFM, DFR & DFT, Components of a CAD package and its highlights, Design Flow considerations; Beginning a circuit design with schematic work and component layout, Demo and examples of layout and routing; Technology file generation from CAD; DFM checklist and design rules; Design for Reliability.

Printed Wiring Board Technologies: Board-level packaging aspects, Review of CAD output files for PCB fabrication, Photo plotting, and mask generation, Process flow-chart; Vias; PWB substrates, Surface preparation, Photoresist and application methods, UV exposure and developing, Printing technologies for PWBs, PWB etching, Resist stripping, Screen-printing technology, Through-hole manufacture process steps, Panel and pattern plating methods, Solder mask for PWBs, Multilayer PWBs; Introduction to microvias, Microvia technology, and Sequential build-up technology process flow for high-density interconnects, Conventional Vs HDI technologies; Flexible circuits.
Surface Mount Technology: SMD benefits; Design issues; Introduction to soldering, Reflow, and Wave Soldering methods to attach SMDs, Solders: Wetting of solders; Flux and its properties, Defects in wave soldering, Vapor phase soldering, BGA soldering, and desoldering/Repair, SMT failures, SMT failure library, Tin Whiskers, Tin-lead, and lead-free solders; Phase diagrams, Thermal profiles for reflow soldering, Lead-free alloys, Lead-free solder considerations; Green electronics; RoHS compliance and e-waste recycling issues.

Thermal Design considerations in systems packaging, Introduction to embedded passives: Need for embedded passives, Design Library, Embedded resistor processes, Embedded capacitors; Processes for embedding capacitors.

NPTEL course (if any): http://nptel/courses/108108031

Text/Reference Books:


Course Outcomes:

At the end of this course, students will demonstrate the ability to

1. Give a comprehensive introduction to the various packaging types used along with the associated thermal, speed, signal, and integrity power issues.
2. Enable the design of packages that can withstand higher temperatures, vibrations, and shock.
3. Design of PCBs that minimize the EMI and operate at a higher frequency
4. Analyze the concepts of Testing and testing methods.

Detailed Contents:

Building blocks for CMOS amplifiers: design of current mirrors, differential amplifiers, CMOS operational transconductance amplifiers: design of single ended telescopic cascode, folded cascode and two-stage amplifiers.

Frequency compensation schemes: Miller compensation, Ahuja compensation and Nested Miller compensation.

Design of fully differential amplifiers, discussion of common mode feedback circuits. Switched capacitor circuits, design of switched capacitor amplifiers and integrators, effect of opamp finite gain, bandwidth and offset, circuit techniques for reducing effects of opamp imperfections, switches and charge injection and clock feed-through effects.
Design of sample and hold and comparators. Fundamentals of data converters; Nyquist rate A/D converters (Flash, interpolating, folding flash, SAR, and pipelined architectures); Nyquist rate D/A converters - voltage, current and charge mode converters, hybrid, and segmented converters); Oversampled A/D and D/A converters.

Design of PLL's and DLL's and frequency synthesizers.

Text/Reference Books:

1. R. Gregorian and Temes - Analog MOS integrated circuits for signal processing.
2. R. Gregorian - Introduction to CMOS opamps and comparators.

Course Outcomes:

At the end of this course, students will demonstrate the ability to

1. understanding of metal-oxide-semiconductor field-effect transistors and the relationship of process technology with models used for analog IC.
2. CMOS digital circuits will be introduced and analyzed. It provides exposure to the complex, non-digital behavior of the devices and circuits with which digital systems are implemented. Emphasis is given on the circuit design, optimization, and layouts.

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<table>
<thead>
<tr>
<th>XCEL8</th>
<th>Bio-Medical Electronics</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

Detailed Contents:


Physiological Pressure and Other Cardiovascular Measurements and Devices: Physiological Pressures, Pressure Measurements, Blood Pressure Measurements Oscillometric, and Ultrasonic Noninvasive Pressure Measurements.
Pressure Amplifier Designs, Ac Carrier Amplifiers, Systolic, Diastolic, and Mean Detector Circuits, Pressure Differentiation (dP/dT) Circuits, Practical Problems in Pressure Monitoring, Step-Function Frequency Response Test, Defibrillator Circuits, Pacemakers.


Text/Reference Books:

Course Outcomes:
At the end of this course, students will demonstrate the ability to
1. An ability to conduct standard tests, measurements, and experiments and to analyze and interpret the result to improve processes.
2. Have adequate knowledge about different types of Electrodes, Transducers, and Amplifiers.
3. Understand the important and modern methods of imaging techniques.

Detailed Contents:
Introduction to digital image processing : What is image processing, Different types of images, Visual perception, Image sensing and Acquisition, Quantization, Sampling, color image processing, Revision of Mathematical concepts for image processing, Intensity transformation, Filtering in spatial and Frequency domain: Image negatives, Log transformations, Histogram processing, Spatial filter: smoothing and Sharpening, Discrete Fourier transform, properties of 2-D DFT, Image smoothing and Sharpening in Fourier domain

Image transforms: Two-dimensional orthogonal and Unitary transforms, Optimum transform, Properties of Unitary transforms, 2D DFT, Cosine transforms, Hadamard transforms, KL transforms, Comparison of image transforms, Edge detection: Gradient and Laplacian based edge detection, Diffusion based edge detection: Isotropic and anisotropic diffusion.

Wavelet transform for Image Processing: Multi resolution expansion, Wavelet functions, Wavelet Series expansion, Continuous and Discrete Wavelet transforms, Wavelet transforms for two-dimensional signals (images), Applications of wavelet transforms for edge extraction, noise suppression.


Text/Reference Books:


Course outcome:

At the end of this course, students will demonstrate the ability to
1. Review the fundamental concepts of a digital image processing system.
2. Analyze images in the frequency domain using various transforms.
3. Evaluate the techniques for image enhancement and image restoration.
4. Categorize various compression techniques.
5. Interpret Image compression standards.
6. Interpret image segmentation and representation techniques.

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<table>
<thead>
<tr>
<th>XCEL11</th>
<th>Low power VLSI</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

Detailed Contents:

Basics of MOS circuits: MOS Transistor structure and device modeling MOS Inverters MOS Combinational Circuits - Different Logic Families.


Supply Voltage Scaling Approaches: Device feature size scaling Multi-Vdd Circuits Architectural level approaches: Parallelism, Pipelining Voltage scaling using high-level transformations Dynamic voltage scaling Power Management

Switched Capacitance Minimization Approaches: Hardware Software Tradeoff Bus Encoding Two’s complement Vs Sign Magnitude Architectural Optimization Clock Gating Logic styles.

Leakage Power minimization Approaches: Variable-threshold-voltage CMOS (VTCMOS) approach
multi-threshold-voltage CMOS (MTCMOS) approach Power gating Transistor stacking Dual-Vt assignment approach (DTCMOS)

NPTEL Course (if any): https://nptel.ac.in/courses/106105034.

Text/Reference Books:


Course outcome:

At the end of this course, students will demonstrate the ability to
1. Capability to recognize advanced issues in VLSI systems, specific to the deep-submicron silicon technologies.
2. Students able to understand deep submicron CMOS technology and digital CMOS design styles.
3. To design chips used for battery-powered systems and high-performance circuits.

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<table>
<thead>
<tr>
<th>XCEL13</th>
<th>Power converters design</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

Detailed Contents:

Review of basic power electronics principles, Introduction to various power electronics supplies. Performance parameters for power electronics supplies and their measurement.

DC to DC converters: Analysis and design of cuk converters, two quadrant and full bridge Non isolated converters, Isolated converters, i.e., flyback, forward, push-pull, half-bridge, full bridge Zeta, and SEPIC topology, block diagram of converter control, modeling such as averaged model, linearized and state space model Design of DC inductor, Concept of integrated magnetic.

Soft switching DC to DC converters, zero current switching topologies, zero voltage switching topologies, generalized switch cell, ZCT and ZVT DC converters, design, and simulation.

Pulse width modulation rectifiers, properties of ideal rectifiers, Realization of near deal rectifiers, CCM boost converter, DCM flyback converters, control of current waveforms, AC Choppers: Modeling and analysis of AC choppers, harmonics control using symmetrical and asymmetrical waveform pattern, design, and simulation.
Static un-interruptible power supply, on-line, off-line and line interactive UPS, modes of operation, batteries and converters selection and design for UPS, performance evaluation of UPS, power factor correction techniques, control of UPS.

**Text/Reference Books:**


**Course outcomes:**

At the end of this course students will demonstrate the ability to

1. Understanding of power electronics concepts, be able to classify DC-DC and AC/DC power electronic converters as per the performance requirement of Power Supplies.
2. Analyse and design conventional DC-DC converter topologies and be capable of developing their mathematical models aiding the steady state and transient analysis.
3. Develop understanding of conventional AC/DC converter topologies and be able to classify and design them based on their power stages, control aspects and other issues like source current quality control etc.
4. Able to classify the different layouts of uninterruptible power supplies, compare the applicable control strategies and identify the various standards followed in this area

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<table>
<thead>
<tr>
<th>XCEL15</th>
<th>Flexible Electronics</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

**Detailed Contents:**

Introduction to Flexible and Printed Electronics: Evolution of Flexible Electronics, review of cutting-edge research on electronics that can be flexible, plastic, stretchable, conformable or printed. Electronic materials, components, and systems, applications for IoT.

Materials, Processing, and Manufacturing: Various semiconductors, dielectric, and conducting materials, Organic semiconductors, from chemical bonds to bands, Charge injection and transport,
Examples of printable functional materials, Thin-film Deposition and Processing Methods for Flexible Devices, Solution-based Patterning Processes; Ink-jet printing, gravure, and other processes, surface energy effects, multilayer patterning.

Flexible Thin-Film Transistors and Circuits: Thin-Film Transistor; Device structure and performance, Electrical characteristics, parameter extraction, characterization methods for rigid and flexible devices, electrical stability, printed transistors; organic/polymer, metal-oxide, electrolyte gated, Case studies; sub micrometer OTFTs and gravure printed OTFTs, From transistors to circuits.

Circuits on flexible and non-silicon substrates, Contacts, and Interfaces to Organic and Inorganic Electronic Devices: Schottky contacts, defects, carrier recombination, the effect of applied mechanical strain.

Other Flexible Devices and System Integration: Organic Light Emitting Diodes, Organic Solar Cells, thin flexible OLED displays, OLED lighting, smart wallpaper, sensors, logic, and memory, RFID tags, Latest applications of printed electronics, Encapsulation, Roll to roll printing processes, Integration Issues, and Designs for the Future.

Text/Reference Books:

4. Large Area and Flexible Electronics, Mario Cairoi & Yong-Young Noh (Editors (2015), WILEY-VCH.

Course Outcomes:

At the end of this course, students will demonstrate the ability to
1. Identify the advantages, drawbacks, performances, complementarity, and uniqueness of large-area manufacturing vs. silicon technology.
2. Integrate the operation principles, architectures, and processing of main devices and systems fabricated for flexible electronics.
3. Predict systems integration issues and propose methods for integration and encapsulation of printed devices and systems.

*****
Detailed Contents:

Overview: Nanodevices, Nano materials, Nano characterization, Definition of Technology node, Basic CMOS Process flow, MOS Scaling theory, Issues in scaling MOS transistors: short channel effects, Description of a typical 65 nm CMOS technology.

Requirements for Nonclassical MOS transistor, MOS capacitor, Role of interface quality and related process techniques, Gate oxide thickness scaling trend, SiO2 vs High-k gate dielectrics. Integration issues of high-k Interface states, bulk charge, band offset, stability, reliability – Qbd high field, possible candidates, CV and IV techniques.


Compound semiconductors – material properties, MESFETs Compound semiconductors MOSFETs in the context of channel quantization and strain, Heterostructure MOSFETs exploiting novel materials, strain, quantization.

Synthesis of Nanomaterials: CVD, Nucleation and Growth, ALD, Epitaxy, MBE. Compound semiconductor hetero-structure growth and characterization: Quantum wells and Thickness measurement techniques: Contact - step height, Optical - reflectance and ellipsometry. AFM. Characterization techniques for nanomaterials: FTIR, XRD, AFM, SEM, TEM, EDAX etc. Applications and interpretation of results. Emerging nano materials: Nanotubes, nanorods and other nano structures, LB technique, soft lithography etc. Microwave-assisted synthesis, Self-assembly etc.

Text/Reference Books:
2. Silicon VLSI Technology, Plummer, Deal, Griffin Pearson Education India.

Course outcomes:
At the end of this course, students will demonstrate the ability to
1. Understand the fundamentals of classical CMOS technology and the issues in scaling MOSFET in the sub-100nm regime.
2. Students are able to analyze the non-classical transistors with new device structures and nanomaterials.
3. Understand the issues in realizing Germanium and compound semiconductor MOSFET and extensive materials characterization techniques that help in designing high-performance transistors.

*****
Detailed Contents:
Semiconductor abrupt junctions, Equilibrium conditions, the contact potential, space charge at a semiconductor junction, qualitative description of current flow at a junction, minority, and majority carrier currents, carrier injection, minority carrier distributions, variation of the quasi-Fermi levels with the position, junction current from excess minority carriers, junction breakdown mechanisms, capacitance of p-n junctions.

Semiconductor heterojunctions: Types of heterojunctions, energy band diagrams of heterostructures, current-voltage and, capacitance-voltage characteristics of anisotype heterojunctions, heterojunction bipolar transistors, electrical and optical characteristics of LEDs, laser gain semiconductor band system, high electron mobility transistor, hot electron heterojunction transistor.

2D electron gas and Quantum wells: 2D electron gas in Si and GaAs MOS structures, effect of applied bias on energy bands of the MOS capacitors, bias dependence of capacitance, free charge carrier transfer.

Triangular quantum wells (both finite and infinite), coupled quantum wells, and superlattices, double heterostructure lasers, single quantum well lasers, multiple quantum well lasers. Optical absorption due to electronic transitions in quantum wells.

Transport properties of heterostructures and quantum devices: Effect of electric field parallel and perpendicular to the interfaces, effects of constant magnetic field, Landau levels, magneto conductivity in a 2D heterostructure. One-D and Zero-D quantum structures.

The density of states in 3D, 2D, 1D and 0D structures, 1D and 0D optical phenomena and optical devices, quantum confined stark effect, quantum well modulators, self-electro-optic effect devices, resonant tunneling devices, the coulomb blockade, single electron transistor.

Text/Reference Books:
5. Luminescence and LED by E. W. Williams and R. Hall.

Course outcomes:
At the end of this course, students will demonstrate the ability to
1. Understand the band structure of heterojunction devices and transport mechanisms.
2. Analyze the electrical behavior of 2D heterojunctions.
3. Understand the density of states in different dimensional structures like 0D, 1D, 2D, and 3D.

*****
Detailed Contents:

An introduction to IoT systems: Introduction and motivation of IoT systems, Hardware components of IoT systems: A quick overview of different components---micro-controllers, SoCs, communication modules, power supply and sensing modules---of off-the-shelf prototyping boards, e.g., Arduino UNO, MSP430 LaunchPad; NodeMCU, STM32.

The software component of IoT systems: Introduction to IDEs for off-the-shelf boards, e.g., Arduino IDE, Waspmote IDE, Code composed studio; Contiki-OS and RIOT OS; 6LowPAN network stack; Sensor interfacing; GPIO programming.

Communication paradigm of IoT systems: Different wireless standards, e.g., IEEE802.15.4, ZigBee, BLE, IEEE802.11; link layer technologies, Medium Access Control; Routing; Application layer protocols; Network topologies.

Performance evaluation of IoT systems: Developing mathematical models for energy consumption, Optimal node placement, and resource allocation over wireless sensor networks to meet QoS requirements. Introduction to Python programming, Introduction to Raspberry Pi, Implementation of IoT with Raspberry Pi. Implementation of IoT with Raspberry Pi (contd), Introduction to SDN, SDN for IoT (contd), Data Handling and Analytics.


NPTEL Course (if any): [https://nptel.ac.in/courses/1061051676](https://nptel.ac.in/courses/1061051676)

Text/Reference Books:


Course outcomes:

At the end of this course, students will demonstrate the ability to

1. students will be familiar with different prototyping boards and their components. They will be able to choose an appropriate board/component for designing an IoT system.
2. Have hands-on experience in programming off-the-shelf boards using respective IDEs. Additionally, they will be able to choose appropriate libraries for interfacing with external sensors, or communication modules.

3. Be versed in different communication standards and technologies. They will be able to choose appropriate communication technology/technologies for designing an IoT system.


5. Able to compare different IoT systems in terms of different performance metrics: network lifetime, power consumption, reliability of the network etc.

6. Able to design a small-scale IoT system for several real-world applications

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<table>
<thead>
<tr>
<th>XCEL19</th>
<th>Cyber security</th>
<th>3L:0T:0P</th>
<th>3 credits</th>
</tr>
</thead>
</table>

Detailed Contents:


Classification of cyber-crimes, Common cyber-crimes- cyber-crime targeting computers and mobiles, cyber-crime against women and children, financial frauds, social engineering attacks, malware and ransomware attacks, zero day and zero click attacks, Cybercriminals modus-operandi , Reporting of cyber-crimes, Remedial and mitigation measures, Legal perspective of cyber-crime, IT Act 2000 and its amendments, Cyber-crime and offences, Organisations dealing with Cyber-crime and Cyber security in India, Case studies.

Introduction to Social networks. Types of Social media, Social media platforms, Social media monitoring, Hashtag, Viral content, Social media marketing, Social media privacy, Challenges, opportunities and pitfalls in online social network, Security issues related to social media, Flagging and reporting of inappropriate content, Laws regarding posting of inappropriate content, Best practices for the use of Social media, Case studies.


End Point device and Mobile phone security, Password policy, Security patch management, Data backup, Downloading and management of third-party software, Device security policy, Cyber Security best practices, Significance of host firewall and Ant-virus, Management of host firewall and Anti-virus, Wi-Fi security, Configuration of basic security policy and permissions.
Text/Reference Books:


Course outline: At the end of this course, students will demonstrate the ability to

1. Understand the concept of Cyber security and issues and challenges associated with it.
2. Understand the cyber-crimes, their nature, legal remedies and as to how report the crimes through available platforms and procedures.
3. Understand various privacy and security concerns on online Social media and understand the reporting procedure of inappropriate content, underlying legal aspects and best practices for the use of Social media platforms.
4. Understand the basic concepts related to E-Commerce and digital payments.
5. Understand the basic security aspects related to Computer and Mobiles. They will be able to use basic tools and technologies to protect their devices.

| XCEL20 | Artificial intelligence and machine learning | 3L:0T:0P | 3 credits |

Detailed Contents:


Automated Reasoning– Formal program techniques: specification by pre- and post-conditions, derivation and verification of programs, invariants. Strategic Reasoning in AI - Agents, strategic behaviours of agents in multiagent systems (MAS) by using the language of alternating-time temporal logic (ATL), an extension of the temporal logics CTL and LTL which allows to express game-theoretical notions such as the existence of a winning strategy for a group of agents - Expert system-based reasoning - Production system, semantic network, and frame - Soft computing based reasoning – Fuzzy logic.
Decision Theory Decision-Making: basics of utility theory, decision theory, sequential decision problems, decision networks, elementary game theory, sample applications; Problem-solving through Search: forward and backward, state-space, blind, heuristic, hill climbing, best-first, A, A*, AO*, minimax, constraint propagation, intelligent search, meta-heuristics, problem-reduction, neural and stochastic; Intelligent agents - reactive, deliberative, goal-driven, utility-driven, and learning agents Artificial Intelligence programming techniques; Planning: planning as search, partial order planning, construction and use of planning graph.


Text/Reference Books:

Course Outcomes:

At the end of this course students will demonstrate the ability to
1. Understand foundation principles, mathematical tools, and program paradigms of AI.
2. Apply intelligent agents for Artificial Intelligence programming techniques.
3. Apply problem solving through search for AI applications.
4. Apply logic and reasoning techniques to AI applications.
5. Develop a good understanding of fundamental principles of machine learning.
7. Develop a model using supervised/unsupervised machine learning algorithms for classification/prediction/clustering.
8. Evaluate performance of various machine learning algorithms on various data sets of a domain.
9. Design and Concrete implementations of various machine learning algorithms to solve a given problem using languages such as Python.

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| XCEL21  | Quantum Computing          | 3L:0T:0P | 3 credits |

Detailed Contents:

Review of Quantum Mechanics and Motivation for Quantum Computation
Qubit: The qubit state - matrix and Bloch sphere representation - computational basis unitary evolution.


Quantum computing: Classical computing using qubits - Quantum parallelism - Deutsch's algorithm -Deutsch Josza algorithm.


Quantum error correcting codes, Physical realization of qubits.

Text/Reference Books:

1. Quantum Computation and Quantum Information, M. A. Nielsen and I. L. Chuang,
Cambridge University Press
2. Quantum Information and Computation, CIT Lecture Notes by J. Preskill.

Course Outcomes:

At the end of this course, students will demonstrate the ability to

1. Students would learn the framework of quantum computation, and how that may be useful for future quantum technologies.

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A Guide to Induction Program
Appendix – II: A Guide to Induction Program

Introduction
In its 49th meeting, held on 14th March 2017, AICTE approved a package of measures for further improving the quality of technical education in the country. This 3-week mandatory Student Induction Program (SIP) based on Universal Human Values (UHV) is one of these key measures.

The SIP is intended to prepare newly admitted undergraduate students for the new stage in their life by facilitating a smooth transition from their home and school environment into the college and university environment.

The present form of the Student Induction Program (SIP) has taken inspiration from and gratefully acknowledges the many efforts in this direction. In particular the Foundation Program at IIT Gandhinagar (July 2011) and the course in Universal Human Values and Professional Ethics (IIIT Hyderabad, 2005; AKTU Lucknow, 2009 and PTU Jalandhar, 2011; overall about 35 universities); and also, the mentorship, internship and apprenticeship programs of several institutions. The SIP amalgamates all the three into an integrated whole, which leads to its high effectiveness in terms of building a healthy lifestyle, creativity, bonding and character. It develops sensitivity towards self and one’s relationships, builds awareness about others and society beyond the individual, and also in bonding with their own batch-mates and senior students as well as faculty members.

The purpose of this document along with accompanying details are to help institutions / colleges in understanding the spirit of the Induction Program and implementing it.

It is in line with the thoughts expressed in the NEP 2020:

“Education is fundamental for achieving full human potential, developing an equitable and just society, and promoting National development”.

“The purpose of the education system is to develop good human beings capable of rational thought and action, possessing compassion and empathy, courage and resilience, scientific temper and creative imagination, with sound ethical moorings and values”.

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1 IIT Gandhinagar places great emphasis on not only educating successful engineers of the future, but also creating well-rounded personalities, who contribute to society, are respectful of and can adapt to their surroundings, and prove themselves to be great thinkers and problem solvers in all avenues of life. In 2011, in line with this vision, It took the bold step to introduce a five week Foundation Program for incoming 1st year UG students. It involved activities such as games, art, etc.; also science and other creative workshops as well as lectures by eminent resource persons. To enable undivided attention on this, normal classes were scheduled only after this program was over.

2 The foundation course was started in 2005 at IIT Hyderabad. In 2009, UP Technical University (now AKTU) introduced it in all academic programs across their 550 colleges. From there on, it has been included in the curriculum of many universities, particularly in technical universities, in quite a natural manner, filling a long-felt need. After AKTU, it was IKG-Punjab Technical University in 2011, then Royal University of Bhutan in 2012 and so on. By 2020, more than 40 universities in India and both universities of Bhutan have been offering this foundation course. Since 2017, it has been a compulsory credit course in AICTE’s model curriculum for all UG courses. Faculty from all departments are involved in conducting the course. The content is universal, rational, verifiable and leading to harmony. The mode is a self-exploration (and not sermonising or lecturing). Faculty are to be prepared beforehand. The results have been quite encouraging.

3 Many institutes setup mentor-mentee network under which 1st year students are divided into small groups, each assigned to a senior student as a Student Buddy, and to a faculty member as a Faculty Mentor. Thus, a new student has their guidance through regular interactions. They can discuss their aims and aspirations as well as concerns whether social, psychological, financial, academic, or otherwise.
“It aims at producing engaged, productive, and contributing citizens for building an equitable, inclusive, and plural society as envisaged by our Constitution”.

“Education must build character, enable learners to be ethical, rational, compassionate, and caring, while at the same time prepare them for gainful, fulfilling employment”.

“The curriculum must include basic arts, crafts, humanities, games, sports and fitness, languages, literature, culture, and values, in addition to science and mathematics, to develop all aspects and capabilities of learners; and make education more well-rounded, useful, and fulfilling to the learner”.

So, when new students join an institution, they are to be welcomed and oriented to the institute, its vision, people, purpose, culture and values, policies, programs, rules and regulations etc. through a well-planned 3-week interaction before regular classes start.

Education aims at developing the students to their full potential, so that they are able to participate meaningfully not only in their profession, but also in their family, society and their natural environment. That requires the development of their values as well as skills.

Engineering colleges were established to train graduates in their respective branch/department of study, be ready for the job market, but also have a holistic outlook towards life and have a desire and competence to work for national needs and beyond. The graduating student must have the knowledge and skills in the area of his study. However, s(he) must also have a broad understanding of society and relationships. Besides the above, several meta-skills and underlying values are needed. Character needs to be nurtured as an essential quality by which s(he) would understand and fulfil his/her responsibility as an engineer, a family member, a citizen etc.

The same applies to all other branches of study – be it professional, vocational or any other area of academic. The graduating student must be a good human being and have the skills in their area of study.

Each family, institution, region, community etc. have evolved their way of life, their cultures over a period of time. The new students are going from one culture to another. Today, a major issue is that one culture tends to be opposed to other cultures. This is because their basic assumptions, and therefore thoughts, are different. Even though there are commonalities at the core value level, the conflict is at the level of expression and details.

With this situation, it is imperative to
- Articulate the essence or core aspects of human culture and civilization, i.e. understand universal human values like trust and respect, love and compassion
- Appreciate the various expressions, different approaches taken in different regions

Our effort is in the context of the whole humanity. However, when it comes to exemplifying these essential concepts, we will have to take to local or national expressions.

In SIP, we want to provide an exposure to essence in the context of the whole humanity first. Then we can take a representative cross-section of all cultures as expressions of this essence. A yardstick to evaluate these various options is provided to guide the student towards a humanistic culture founded on the truth and universal human values like love and compassion.
For example: We want to live with fulfilment as a society. This part is common, universal. To exemplify this, we may expose students to traditional Indian culture and philosophy as well as contemporary western culture and thought.

The intent is:

- Connecting the basic principles through specific examples
- To see and appreciate various cultures, to see the commonality amongst them, in the light of clarity about human culture and civilisation.
- To evaluate any specific example, system or culture, with a view to fill the gaps, rather than to criticise or reject it. Further, we can also be mutually enriching for other cultures.

Student Induction Program (SIP)

With this background, the SIP has been formulated with specific goals to help students to:

- Become familiar with the ethos and culture of the institution (based on institutional culture and practices)
- Set a healthy daily routine, create bonding in batch as well as between faculty members and students
- Get an exposure to a holistic vision of life, develop awareness, sensitivity and understanding of the Self---family---Society---Nation---International---Entire Nature
- Facilitate them in creating new bonds with peers and seniors who accompany them through their college life and beyond
- Overcome weaknesses in some essential professional skills – only for those who need it (e.g. Mathematics, Language proficiency modules)

The SIP consists of different activities which includes meeting new students, socializing with teachers and other people in the university. Secondly associating with the Local area or city, knowing different departments, associating with the department heads, local stores and necessary shops for the survival at new place. Basically, getting information about the rules and regulations of the university which includes do's and don’ts. Other activities which may involve students in several creative, cultural and co-curricular activities through which they can explore themselves and get idea about their intrinsic desires and interests which may help them in the long run. In order to make it worth, at the initial level of joining of student various seminars, lectures by eminent personalities, sessions by the appointed mentor for the student is being done to make them more familiar with the university environment. It has been seen that student after schooling when moves towards further studies for either undergraduate or post-graduation has got so many confusions and false knowledge about the college and the curriculum. They should know the basic idea about the fruits and prospects of the particular course and the university or institute in which they are entering. To have faith about their choices and to know that after completion, they will be well equipped with the values and skills which may aid to their future goals and let them work for their personal motives, society and the Nation’s development.

The various modules or core areas recommended for the 3-week SIP are:

**SIP Module 1: Universal Human Values I (UHV I) 22 hours**

The purpose is to help develop a holistic perspective about life. A self-reflective methodology of teaching is adopted. It opens the space for the student to explore his/her
role (value) in all aspects of living – as an individual, as a member of a family, as a part of the society and as a unit in nature. Through this process of self-exploration, students are able to discover the values intrinsic in them. The session-wise topics are given below:

<table>
<thead>
<tr>
<th>Session No.</th>
<th>Topic Title</th>
<th>Aspirations and Issues</th>
<th>Basic Realities (underlying harmony)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Welcome and Introductions</td>
<td>Getting to know each other</td>
<td>Self-exploration</td>
</tr>
<tr>
<td>2 and 3</td>
<td>Aspirations and Concerns</td>
<td>Individual academic, career... Expectations of family, peers, society, nation... Fixing one’s goals</td>
<td>Basic human aspirations Need for a holistic perspective Role of UHV</td>
</tr>
<tr>
<td>4 and 5</td>
<td>Self-Management</td>
<td>Self-confidence, peer pressure, time management, anger, stress... Personality development, self-improvement...</td>
<td>Harmony in the human being</td>
</tr>
<tr>
<td>6 and 7</td>
<td>Health</td>
<td>Health issues, healthy diet, healthy lifestyle Hostel life</td>
<td>Harmony of the Self and Body Mental and physical health</td>
</tr>
<tr>
<td>8, 9, 10 and 11</td>
<td>Relationships</td>
<td>Home sickness, gratitude towards parents, teachers and others Ragging and interaction Competition and cooperation Peer pressure</td>
<td>Harmony in relationship Feelings of trust, respect... gratitude, glory, love</td>
</tr>
<tr>
<td>12</td>
<td>Society</td>
<td>Participation in society</td>
<td>Harmony in the society</td>
</tr>
<tr>
<td>13</td>
<td>Natural Environment</td>
<td>Participation in nature</td>
<td>Harmony in nature/existence</td>
</tr>
<tr>
<td>14</td>
<td>Sum Up</td>
<td>Review role of education Need for a holistic perspective</td>
<td>Information about UHV-II course, mentor and buddy</td>
</tr>
<tr>
<td>15</td>
<td>Self-evaluation and Closure</td>
<td>Sharing and feedback</td>
<td></td>
</tr>
</tbody>
</table>

**SIP Module 2: Physical Health and Related Activities** 51 hours
This module is intended to help understand the basic principles to remain healthy and fit and practice them through a healthy routine which includes exercise, games etc.

**SIP Module 3: Familiarization of Department/Branch and Innovation** 06 hours
This module is for introducing and relating the student to the institution/department/branch; how it plays a role in the development of the society, the state, region, nation and the world at large and how students can participate in it.

**SIP Module 4: Visit to a Local Area** 10 hours
To relate to the social environment of the educational institution as well as the area in which it is situated through interaction with the people, place, history, politics...

**SIP Module 5: Lectures by Eminent People**  
06 hours  
Listening to the life and times of eminent people from various fields like academics, industry etc. about careers, art, self-management and so on enriches the student’s perspective and provides a holistic learning experience.

**SIP Module 6: Proficiency Modules**  
06 hours  
This module is to help fill the gaps in basic competency required for further inputs to be absorbed. It includes effort to make student proficient in interpersonal communication and expression as well as awareness about linguistic and thereafter NLP.

**SIP Module 7: Literature / Literary Activities**  
30 hours  
Through the exposure of local, national and international literature, this module is aimed at helping the student learn about traditional as well as contemporary values and thought.

**SIP Module 8: Creative Practices**  
49 hours  
This module is to help develop the clarity of humanistic culture and its creative, joyful expression through practice of art forms like dance, drama, music, painting, pottery, sculpture etc.

**SIP Module 9: Extra Curricular Activities**  
06 hours  
This is a category under which things that are not placed in any of the above may be placed. Some clubs and hobby group may be made for each of the above categories, so that students may pursue them even after SIP.

The recommended hours to be allocated are given above. Depending on the available faculty, staff, infrastructure, playgrounds, class timings, hostellers and day scholars etc., the timetable for these activities may be drawn up. Of course, colleges may conduct an inaugural function at the beginning of the SIP; and they may also conduct a celebratory closing ceremony at the end of the SIP.

In particular, during the lockdown phase, appropriate care may be taken and some or all activities may be planned in distance-learning or on-line mode.

**Sample 3-week Activity List**

<table>
<thead>
<tr>
<th>Week 1</th>
<th>Inaugural Function</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Regular SIP Activities (See Hours Plan)</td>
</tr>
<tr>
<td>Week 2</td>
<td>Regular SIP Activities (See Hours Plan)</td>
</tr>
<tr>
<td>Week 3</td>
<td>Regular SIP Activities (See Hours Plan)</td>
</tr>
<tr>
<td></td>
<td>Valedictory and Closing Ceremony (Celebration)</td>
</tr>
</tbody>
</table>

**Implementation**

Every institution/college is expected to conduct the 3-week SIP under the guidance of the Director/Principal or Dean Students or a senior faculty member. For this, the institution is
expected to make an SIP Cell / team, which will be responsible for planning, and then implementation of the SIP.

A UHV Cell is expected to be set up at each college and university. At the college, it will be managed by the UHV Convener / Coordinator under the chairpersonship of the director/principal. Faculty members and some students will be the members. They will coordinate the UHV activities like UHV-I during SIP, UHV-II, the faculty mentoring program and student buddy program throughout the student’s association with the institute/college. The UHV Cell will work to incorporate human values in every aspect of education at the institute/college. Preparing UHV Faculty (Mentors) is one of its important activities.

Follow up

The SIP is only the beginning of the interaction with newly joined students.

An important part of the SIP is to associate one faculty mentor to every small groups of about 20 students; and also associate one senior student buddy to an even smaller groups of about 5 students for the guidance required for holistic development of the newly joined student throughout his/her time in the institution/college.

These activities are to be continued in the ongoing academic program along with other cultural activities through various student clubs which are largely be managed by students with the help of one or more faculty mentors. One of the main responsibilities of the faculty mentors would be helping the clubs to review their activities in alignment with human values.

Assessing the Implementation and Impact

The institution / college is expected to take feedback and prepare appropriate reports for assessing the impact and for further improvement of SIP. The basic feedback forms are included with the SIP Teaching Materials.

The SIP and its further follow up is expected to positively impact common graduate attributes like:

- Holistic vision of life
- Socially responsible behaviour
- Environmentally responsible work
- Ethical human conduct

Having Competence and Capabilities for Maintaining Health and Hygiene

Appreciation and aspiration for excellence (merit) and gratitude for all

AICTE will conduct periodic assessment to ascertain the implementation efforts and impact of the SIP and related activities.

Faculty Development

To ensure the implementation of SIP, and in particular to prepare the faculty, the National Coordination Committee for Student Induction (NCC-IP) has been formed. It offers various faculty development programs (FDPs) with the support from AICTE HQ and Regional Offices.

**UHV Faculty (Mentors):** Every institution is expected to prepare UHV Faculty in the ratio of 1:20 (1 faculty per 20 newly admitted students). Faculty from every teaching
Faculty for other Modules: Institutions/colleges generally have faculty, coaches, student clubs, alumni for these areas. FDP and comprehensive material will also be made available.

Student Activity Cell (SAC) – SIP Cell, UHV Cell and Fostering Unit

Student Activity Cell will have three cells or units:
- Fostering Unit – for coordinating various student clubs and activities in alignment with human values and IKS through various student clubs
- SIP Cell – for coordinating the annual SIP
- UHV Cell – for coordinating regular UHV activities, including UHV-I during SIP and UHV-II during future semesters, faculty mentoring and student buddy programs etc.

Each cell / unit will have some axis. E.g. the Fostering Unit will have 3 axis:
- UHV Axis – for UHV inputs and activities after the SIP
- Health Axis – for health oriented inputs and activities after SIP
- Career Axis – for career related inputs

Each axis will have one or more dimensions. E.g. the UHV Axis will have two dimensions:
- UHV Dimension
- Social Work Dimension

Details of the clubs will be based on local conditions.

Director or Principal or Dean of Student affairs will be the Chairman of Student Activity Cell

SIP Cell (or Induction Unit) will be managed by faculty members with the help of student volunteers. 5 to 7 faculty members will be the members. The SIP Cell
will be responsible for planning, organization, coordination and reporting of the annual Student Induction Program with the help of other faculty members and student volunteers

- UHV Cell will be managed by the UHV Convener / Coordinator under the chairpersonship of the director/principal. Faculty members and some students will be the members. They will coordinate the UHV activities like UHV-I during SIP, UHV-II 3rd/4th semester, faculty mentoring program and student buddy program throughout the student’s association with the institute/college. UHV Cell will work to incorporate human values in every aspect of education at the institute/college. Preparing UHV Faculty (Mentors) is one of its activities

- Fostering unit will largely be managed by students with the help of one fostering unit faculty mentor. Student will be coordinators for axis, dimensions and clubs. Fostering unit will take support from induction unit as and when required. It will be responsible for coordinating various student clubs and activities in alignment with human values and Indian Knowledge System

**SIP Teaching Material and More Details**

The SIP Handbook as well as detailed guides and material for each of the modules is available on the AICTE website (http://www.fdp-si.aicte-india.org/download.php).

**Details and Reference Documents:**
- G012 SIP Handbook v2
- Teaching Material for UHV-I v2.1
- Teaching Material for SIP modules 2 to 9 v1
- G008 Facilitator (Mentor) Manual Version 2.1
- G911 UHV Cell, Nodal and Resource Centres
- G009 RP Development Process v2

**#Note: For CSE UG Students only**

The Department of Telecommunications, Ministry of Communication, Government of India is going to auction 5G spectrum shortly. The adoption of 5G will accelerate employment generation in telecom and technology industry. The 5G Technology will penetrate the entire telecom ecosystem of hardware, software and services that are critical for implementation of other futuristic technologies like Internet of Thing (IoT), Machine-to-Machine (M2M) communication, edge computing etc. Innovative applications in various sectors like agriculture, transportation, power etc. will use and requires knowledge of inherent features of 5G. There will be huge requirement of market ready talent pool in 5G technology.

Considering the need for specialized courses and modules on 5G Technology, National Telecommunication Institute for Policy & Research, Innovation & Training (NTIPRIT)-Department of Telecommunication, after due consultation with academia and industry, sent a proposal to AICTE vide No. 1-3/2020-NTI.TS-SD dated 09.03.2021 to include the following:
- A full Semester course on "Advanced Mobile Communications" for UG
- A 14-hour 5G awareness Program for UG Students;
5G Awareness Programme for UG students (14 hours)

Course Title: Introduction to 5G

Topics to be covered

1. IMT2020 enhancements in comparison to IMT Advanced
2. 5G potential and applications
3. Usage scenarios: eMBB, URLLC, MMTC
4. Spectrum for 5G and spectrum sharing
5. Millimeter wave communication and small cells
7. Massive MIMO and beam forming
8. Multi-access edge computing
9. Software defined networks
10. Network slicing
11. Current state of deployment
12. Large cell scenarios: LMLC

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